

# SM7302ESKP-VB Datasheet Dual N-Channel 30V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}$ ( $\Omega$ )	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)		
30	$0.0034 \text{ at V}_{GS} = 10 \text{ V}$	60	17 nC		
	$0.0043  \text{at V}_{GS} = 4.5  \text{V}$	55	17 110		

#### **FEATURES**

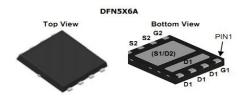
- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFET
- 100 % UIS Tested
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC

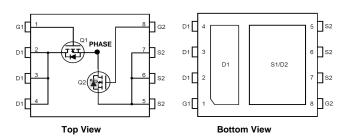


ROHS COMPLIANT HALOGEN FREE

#### **APPLICATIONS**

- Set Top Box
- Low Current DC/DC





Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	30	V
Gate-Source Voltage		$V_{GS}$	± 20	V
-	T <sub>C</sub> = 25 °C		60 <sup>a</sup>	
Continuous Drain Current (T <sub>.1</sub> = 150 °C)	T <sub>C</sub> = 70 °C	I <sub>D</sub>	50	
(.j )	$T_A = 25 ^{\circ}C$	, o	25.2 <sup>b, c</sup>	
	T <sub>A</sub> = 70 °C		22.2 <sup>b, c</sup>	Α .
Pulsed Drain Current		I <sub>DM</sub>	200	
Castinuara Carras Brain Binda Corrant	T <sub>C</sub> = 25 °C	l-	2.25	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	ls –	1.48 <sup>b, c</sup>	
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	5	
Single Pulse Avalanche Energy	e Energy		1.25	mJ
Maximum Power Dissipation	T <sub>C</sub> = 25 °C		56	
	T <sub>C</sub> = 70 °C	P <sub>D</sub>	15	W
	T <sub>A</sub> = 25 °C		2.18 <sup>b, c</sup>	VV
	T <sub>A</sub> = 70 °C		1.34 <sup>b, c</sup>	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a, c, d</sup>	t ≤ 10 s	R <sub>thJA</sub>	58	70	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>th.IF</sub>	38	45	C/ VV	

#### Notes:

- a. Package limited, T<sub>C</sub> = 25 °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s
- d. Maximum under Steady State conditions is 110  $^{\circ}\text{C/W}.$



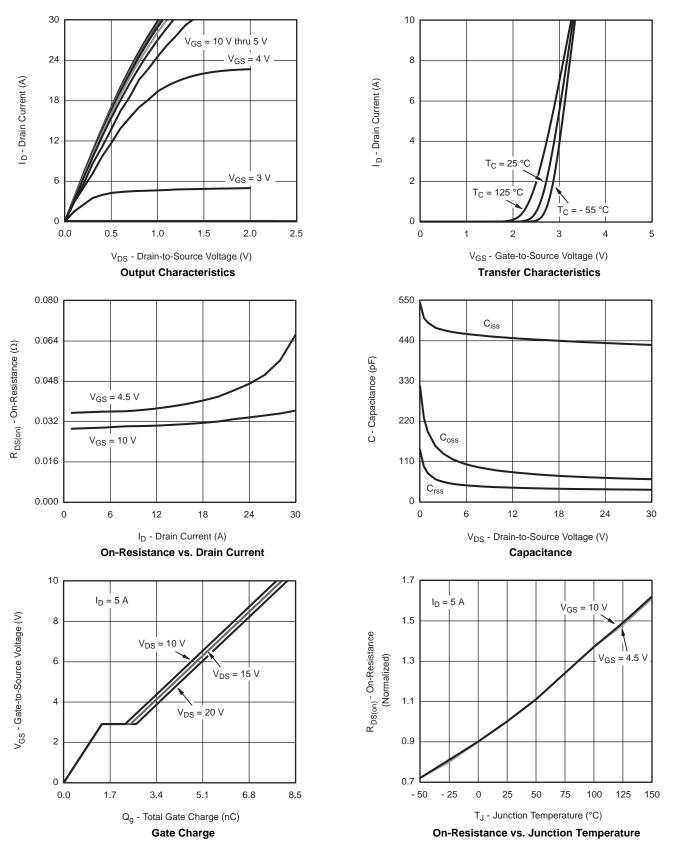
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static				<u>'</u>		•
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = 250 μA		32		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 5.0		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	1.0		2.5	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1	μА
		V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	10			Α
Drain-Source On-State Resistance <sup>a</sup>	` '	$V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$		0.0034		Ω
	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_{D} = 4 \text{ A}$		0.0043		
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	$V_{DS} = 10 \text{ V}, I_D = 5 \text{ A}$		16		S
Dynamic <sup>b</sup>		-		1		<u> </u>
Input Capacitance	C <sub>iss</sub>			1300		pF
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		257		
Reverse Transfer Capacitance	C <sub>rss</sub>			155		
Total Gate Charge	Q <sub>g</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$		17		nC
				3.7	5.6	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 5 \text{ A}$		1.4		
Gate-Drain Charge	Q <sub>gd</sub>			1.05		
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.8	4.3	8.6	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			12	24	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 3 $\Omega$		55	100	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong 5$ A, $V_{GEN}$ = 4.5 V, $R_g$ = 1 $\Omega$		11	22	
Fall Time	t <sub>f</sub>			8	16	
Turn-On Delay Time	t <sub>d(on)</sub>			4	8	- ns -
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 3 $\Omega$		9	18	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 5$ A, $V_{GEN} = 10$ V, $R_g = 1$ $\Omega$		10	20	
Fall Time	t <sub>f</sub>			6	12	
<b>Drain-Source Body Diode Characteristi</b>	cs					
Continuous Source-Drain Diode Current	I <sub>S</sub>	$T_C = 25  ^{\circ}C$			2.25	А
Pulse Diode Forward Current	I <sub>SM</sub>				24	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 2 A, V <sub>GS</sub> = 0 V		0.8	1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			11	20	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 5 A, dI/dt = 100 A/μs, T <sub>J</sub> = 25 °C		4	8	nC
Reverse Recovery Fall Time	t <sub>a</sub>	1 <sub>F</sub> = 0 Λ, αι/αι = 100 Α/μs, 1 <sub>J</sub> = 25 C		7		200
Reverse Recovery Rise Time	t <sub>b</sub>	7		4		ns

#### Notes:

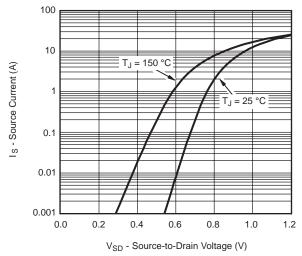
- a. Pulse test; pulse width  $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

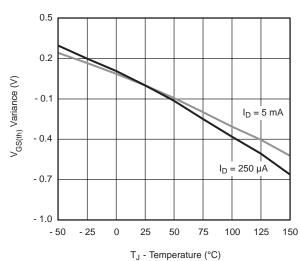




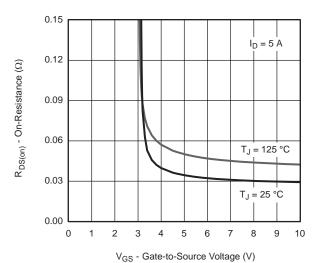




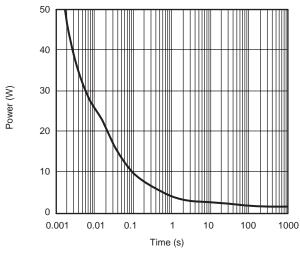
#### Source-Drain Diode Forward Voltage



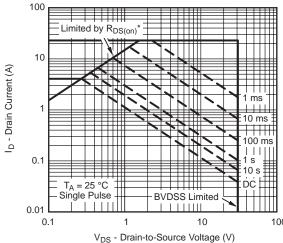
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



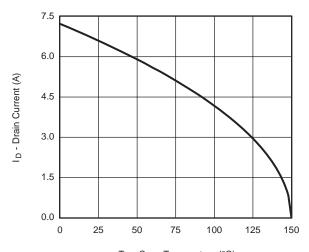
Single Pulse Power



\* V<sub>GS</sub> > minimum V<sub>GS</sub> at which R<sub>DS(on)</sub> is specified

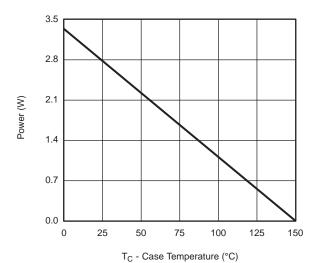
Safe Operating Area, Junction-to-Ambient

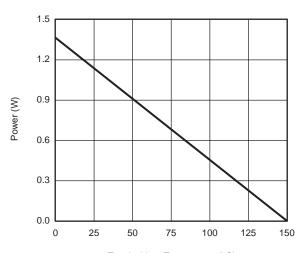




T<sub>C</sub> - Case Temperature (°C)







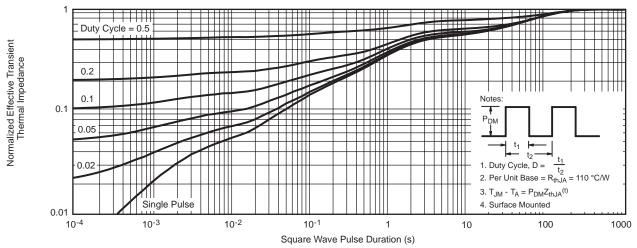
T<sub>A</sub> - Ambient Temperature (°C)

Power, Junction-to-Ambient

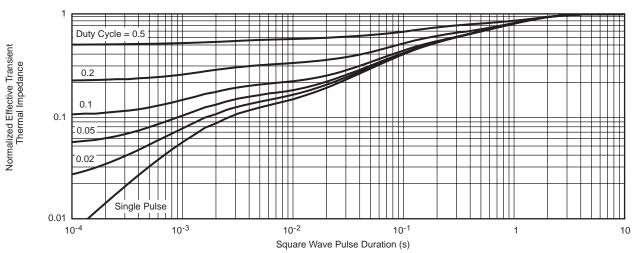
Power, Junction-to-Foot

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



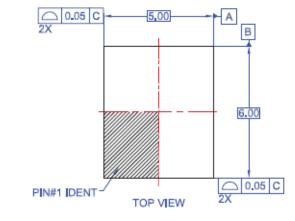


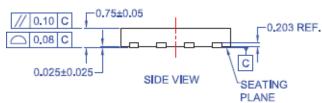
Normalized Thermal Transient Impedance, Junction-to-Ambient

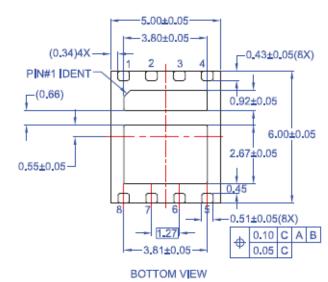


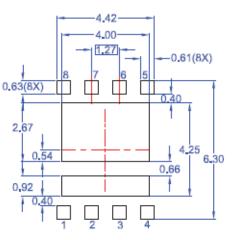
Normalized Thermal Transient Impedance, Junction-to-Foot











RECOMMENDED LAND PATTERN

#### NOTE:

- PACKAGE DOES NOT FULLY CONFORM TO JEDEC STANDARD.
- B. DIMENSIONS ARE IN MILLIMETERS.
- DIMENSIONS AND TOLERANCES PER ASME Y14.5M,
- LAND PATTERN RECOMMENDATION IS EXISTING INDUSTRY LAND PATTERN.
- E. DRAWING FILENAME: MKT-MLP08Krev3.



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