

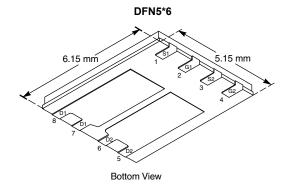
# SIR770DP-T1-GE3-VB Datasheet Dual N-Channel 30 V (D-S) MOSFET

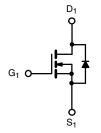
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)			
30	0.018 at V <sub>GS</sub> = 10 V	22			

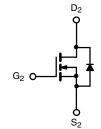
#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFET
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC









N-Channel MOSFET

N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted)						
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30		V	
Gate-Source Voltage		$V_{GS}$	± 20		V	
Continuous Drain Current (T <sub>.I</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C		22			
Continuous Diam Current (1, = 150 °C)	T <sub>A</sub> = 70 °C	I <sub>D</sub>	15		А	
Pulsed Drain Current		I <sub>DM</sub>	50		A	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	2.9			
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	D.	3.5 2.2		W	
Maximum Fower Dissipation	T <sub>A</sub> = 70 °C	P <sub>D</sub>			VV	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55	to 150	°C	
Soldering Recommendations (Peak Temperature)				260	C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maniero de La Alianda Andrianda	t ≤ 10 s	B	26	35	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	R <sub>thJA</sub>	60	85	°C/W
Maximum Junction-to-Case (Drain)	Steady State	$R_{thJC}$	3.9	5.5	

#### Notes:

a. Surface mounted on 1" x 1" FR4 board.

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1



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.8		2.4	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zava Cata Valtaga Dvain Current		V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V	) V		1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			5	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α	
Drain-Source On-State Resistance <sup>a</sup>	_	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A		0.018			
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 8.5 A		0.024		Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 10 A		22		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = 2.9 A, V <sub>GS</sub> = 0 V		0.75	1.2	V	
Dynamic <sup>b</sup>							
Total Gate Charge	Qg			13	20		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 10 \text{ A}$		2		nC	
Gate-Drain Charge	Q <sub>gd</sub>			2.7			
Gate Resistance	R <sub>g</sub>		0.5		3.2	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			8	16		
ise Time t <sub>r</sub>		$V_{DD} = 15 \text{ V, R}_{L} = 15\Omega$		10	20		
Turn-Off Delay Time	t <sub>d(off)</sub>	$t_{d(off)}$ $I_D \cong 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 6 \Omega$		21	40	ns	
Fall Time	t <sub>f</sub>			10	20		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 2.9 A, dI/dt = 100 A/μs		40	80		

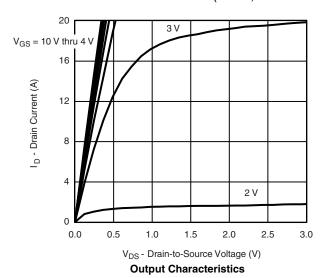
#### Notes:

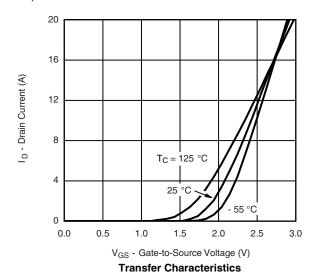
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- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

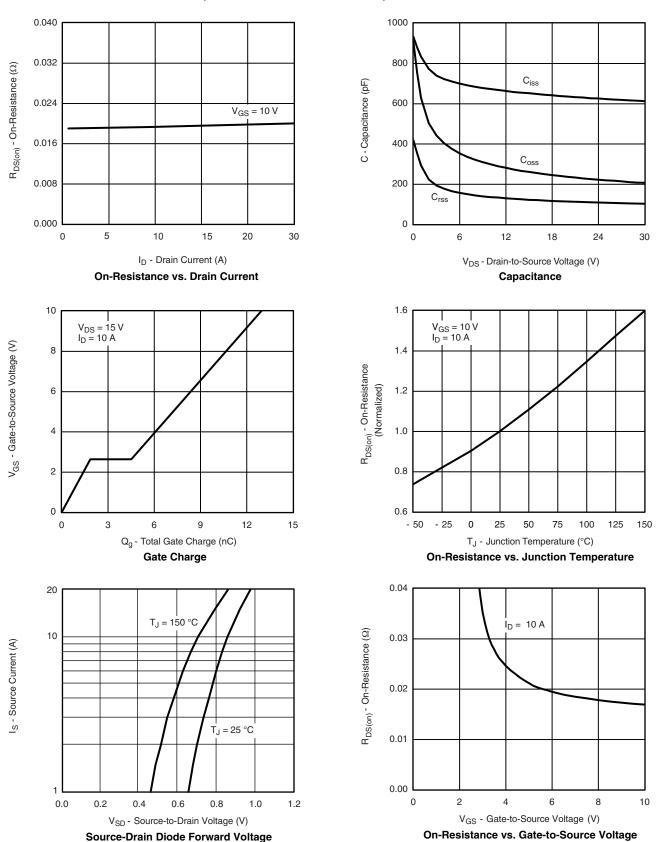
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)







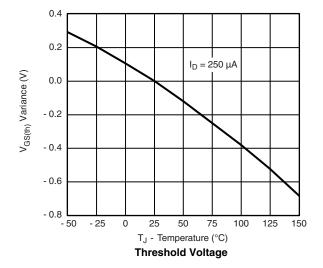
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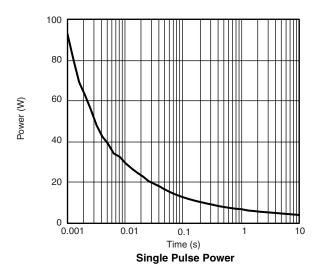


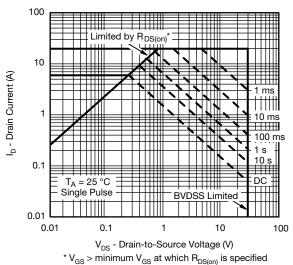
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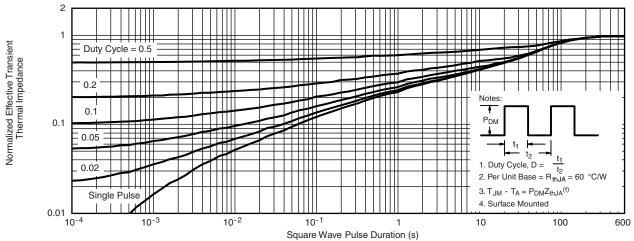
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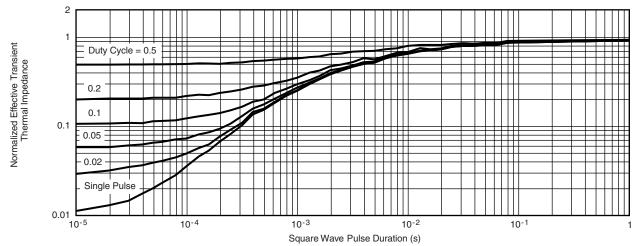


Normalized Thermal Transient Impedance, Junction-to-Ambient

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## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



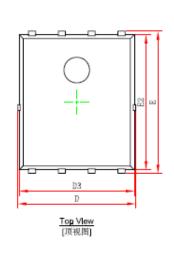
Normalized Thermal Transient Impedance, Junction-to-Case

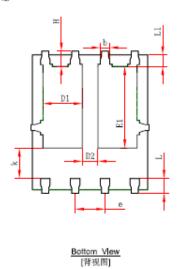
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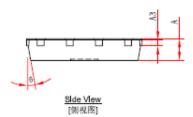


6

#### PDFNWB5×6-8L-A PACKAGE OUTLINE DIMENSIONS







Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	0.900	1.000	0.035	0.039	
A3	0.254	REF.	0.010	REF.	
D	4.944	5.096	0.195	0.201	
E	5.974	6.126	0.235	0.241	
D1	1.470	1.870	0.058	0.074	
D2	0.470	0.870	0.019	0.034	
E1	3.375	3.575	0.133	0.141	
D3	4.824	4.976	0.190	0.196	
E2	5.674	5.826	0.223	0.229	
k	1.190	1.390	0.047	0.055	
b	0.350	0.450	0.014	0.018	
е	1.270TYP.		0.050	TYP.	
L	0.559	0.711	0.022	0.028	
L1	0.424	0.576	0.017	0.023	
Н	0.574	0.726	0.023	0.029	
θ	10°	12°	10°	12°	

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