

VSB006P02LS-VB Datasheet P-Channel 20-V (D-S) MOSFET

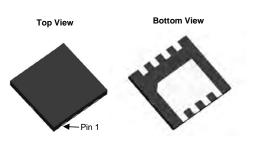
PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)	Q _g (Typ.)		
- 20	0.0040 at V _{GS} = 10 V	- 52	21.5 nC		
- 20	0.0060 at V_{GS} = 4.5 V	- 40	21.5110		

FEATURES

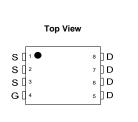
- Halogen-free According to IEC 61249-2-21
 Definition
- Trench Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC

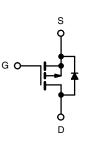
APPLICATIONS

- · Load Switch
- · Adaptor/Battery Switch



DFN 3x3 EP





P-Channel MOSFET

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	- 20	V	
Gate-Source Voltage		V _{GS}	± 16		
Continuous Drain Current (T _J = 150 °C)	$T_{C} = 25 °C$ $T_{C} = 70 °C$ $T_{A} = 25 °C$ $T_{A} = 70 °C$	I _D	- 52 - 40 ^g - 26 ^{b, c} - 21 ^{b, c}	A	
Pulsed Drain Current		I _{DM}	- 150		
Continuous Source-Drain Diode Current	T _C = 25 °C T _A = 25 °C	I _S	- 40 ^g - 4.5 ^{b, c}		
Maximum Power Dissipation	$T_{C} = 25 °C$ $T_{C} = 70 °C$ $T_{A} = 25 °C$ $T_{A} = 70 °C$	P _D	54 34.7 5.0 ^{b, c} 3.2 ^{b, c}	w	
Operating Junction and Storage Temperature R	T _J , T _{stg}	- 55 to 150	°C		
Soldering Recommendations (Peak Temperatur		260			

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, f}	t ≤ 10 s	R _{thJA}	20	25	°C/W
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	1.8	2.3	0/11

Notes:

a. Based on $T_C = 25 \ ^{\circ}C$.

b. Surface Mounted on 1" x 1" FR4 board.

c. t = 10 s.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static						•	
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0, I_{D} = -250 \ \mu A$	- 20			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = - 250 μA		- 15		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	i _D = - 250 μA		4.5			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1		- 2.2	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 16 V$			± 100	nA	
		$V_{DS} = -20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	μA	
Zero Gate Voltage Drain Current	IDSS	V_{DS} = - 20 V, V_{GS} = 0 V, T_{J} = 55 °C			- 10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 V, V_{GS} = -10 V$	- 30			A	
		V _{GS} = - 10 V, I _D = - 26 A		0.0040		1	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 21 A		0.0060		Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 26 A		58		S	
Dynamic ^b							
Input Capacitance	C _{iss}			4595			
Output Capacitance	C _{oss}	V _{DS} = - 10 V, V _{GS} = 0 V, f = 1 MHz		910		pF	
Reverse Transfer Capacitance	C _{rss}			813			
		V _{DS} = - 10 V, V _{GS} = - 10 V, I _D = - 20 A		95.3	143		
Total Gate Charge	Qg		46.5 7	70	1		
Gate-Source Charge	Q _{gs}	V_{DS} = - 10 V, V_{GS} = - 4.5 V, I_{D} = - 20 A		13.7		- nC	
Gate-Drain Charge	Q _{gd}			12.5			
Gate Resistance	Rg	f = 1 MHz	0.4	1.9	3.8	Ω	
Turn-On Delay Time	t _{d(on)}			19	30	-	
Rise Time	t _r	V_{DD} = - 10 V, R_L = 1 Ω		10	20		
Turn-Off Delay Time	t _{d(off)}	I_D \cong - 10 A, V_{GEN} = - 10 V, R_g = 1 Ω		65	98		
Fall Time	t _f			13	20	1	
Turn-On Delay Time	t _{d(on)}			55	83	ns	
Rise Time	t _r	V_{DD} = - 10 V, R_L = 1 Ω		52	78	-	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ - 10 A, V_GEN = - 4.5 V, R_g = 1 Ω		53	80		
Fall Time	t _f			25	38		
Drain-Source Body Diode Characteris	tics						
Continuous Source-Drain Diode Current	ا _S	T _C = 25 °C			- 40	•	
Pulse Diode Forward Current ^a	I _{SM}				- 70	A	
Body Diode Voltage	V _{SD}	I _S = - 1 A		- 0.74	- 1.1	V	
Body Diode Reverse Recovery Time	t _{rr}			42	63	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			25	38	nC	
Reverse Recovery Fall Time	t _a	$I_F = -10 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, \text{ T}_J = 25 ^\circ\text{C}$		12			
Reverse Recovery Rise Time t _b				30		ns	

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



T_C = - 55 °C

2.5

3.0

 $T_C = 25 \degree C$

1.5

10

Capacitance

0.7

- 50

- 25

0

25

50

T_J - Junction Temperature (°C) On-Resistance vs. Junction Temperature

75

100

125

150

15

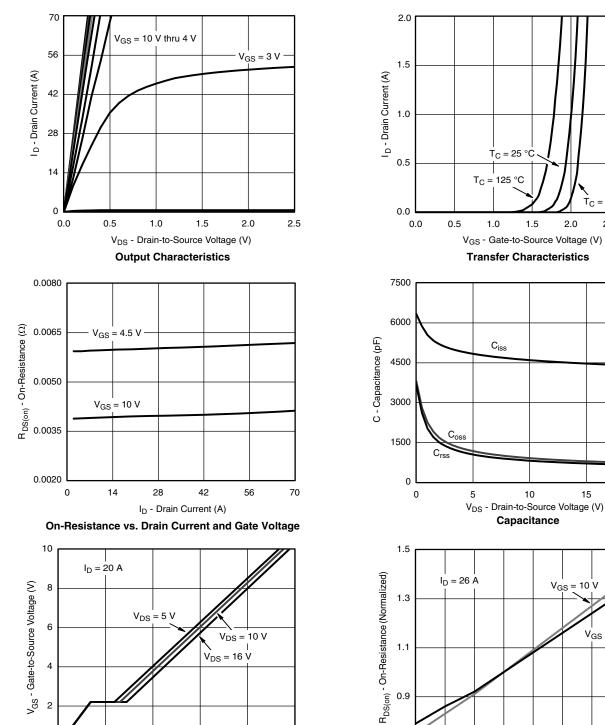
 $V_{GS} = 4.5 V$

V_{GS} = 10 V 20

2.0

1.0

Ciss



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

0

0

20

40

60

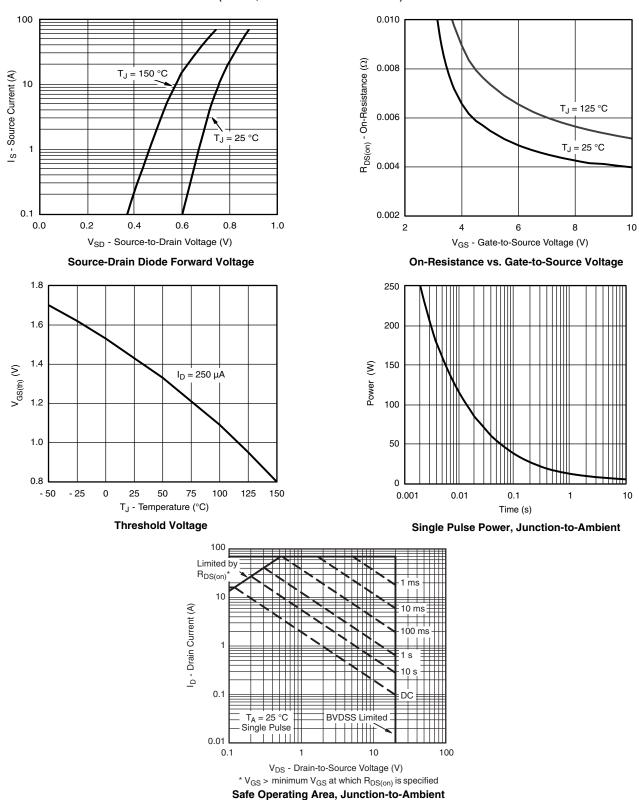
Qg - Total Gate Charge (nC)

Gate Charge

80

100

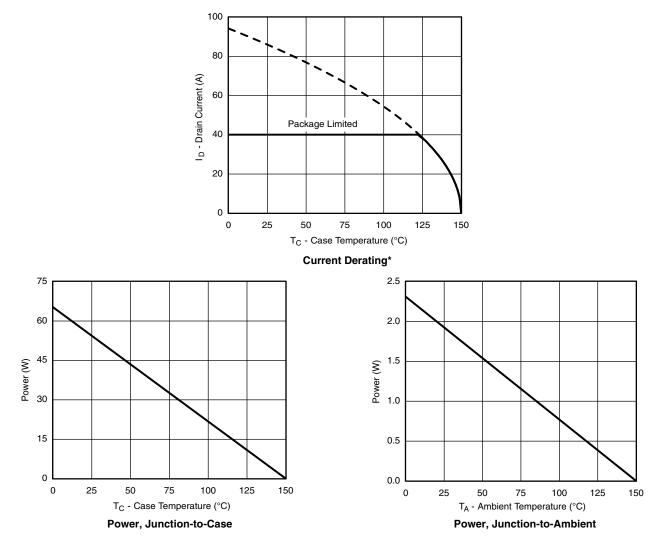




TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



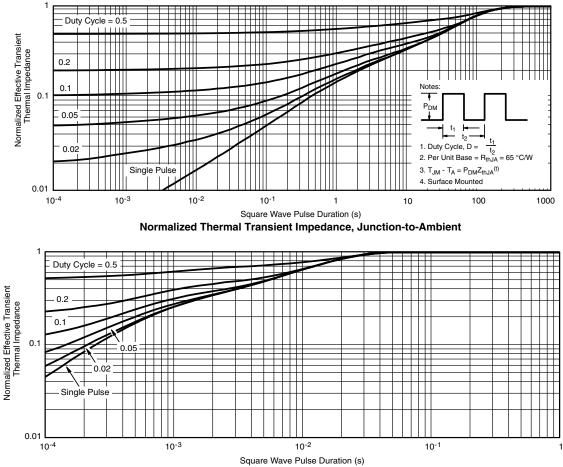
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

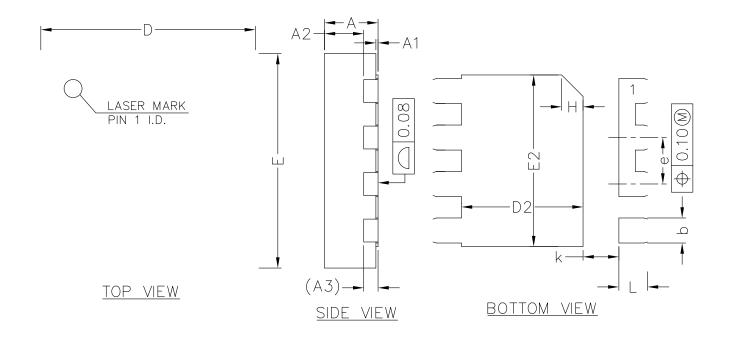


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case







<u>SIDE VIEW</u>

SYMBOL	MIN	NOM	MAX	
А	0.70	0.75	0.80	
A1	0.00	0.02	0.05	
A2	0.50	0.55	0.60	
A3	0.20REF			
b	0.30	0.35	0.40	
D	2.90	3.00	3.10	
E	2.90	3.00	3.10	
D2	1.60	1.70	1.80	
E2	2.30	2.40	2.50	
е	0.55	0.65	0.75	
K	0.40	0.50	0.60	
L	0.35	0.40	0.45	

COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)



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