

## SSPS7333P-VB Datasheet

### P-Channel 30 V (D-S) MOSFET

$V_{DS}$		-30	V
$R_{DS(on),typ}$	$V_{GS}=10V$	11	$m\Omega$
$R_{DS(on),typ}$	$V_{GS}=4.5V$	18	$m\Omega$
$I_D$		-45	A

#### FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFET
- Low Thermal Resistance Power Package with Small Size and Low 1.07 mm Profile
- 100 %  $R_g$  and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

#### APPLICATIONS

- Load Switch
- Adaptor Switch
- Notebook PC



#### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	- 30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current ( $T_J = 150\text{ }^{\circ}\text{C}$ )	$I_D$	$T_C = 25\text{ }^{\circ}\text{C}$	- 45
		$T_C = 70\text{ }^{\circ}\text{C}$	- 30
		$T_A = 25\text{ }^{\circ}\text{C}$	- 14.4 <sup>a, b</sup>
		$T_A = 70\text{ }^{\circ}\text{C}$	- 11.5 <sup>a, b</sup>
Pulsed Drain Current	$I_{DM}$	- 60	A
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25\text{ }^{\circ}\text{C}$	- 35 <sup>e</sup>
		$T_A = 25\text{ }^{\circ}\text{C}$	- 3.2 <sup>a, b</sup>
Avalanche Current	$I_{AS}$	- 25	
Single-Pulse Avalanche Energy	$E_{AS}$	31.25	mJ
Maximum Power Dissipation	$P_D$	$T_C = 25\text{ }^{\circ}\text{C}$	52
		$T_C = 70\text{ }^{\circ}\text{C}$	43
		$T_A = 25\text{ }^{\circ}\text{C}$	3.8 <sup>a, b</sup>
		$T_A = 70\text{ }^{\circ}\text{C}$	2.4 <sup>a, b</sup>
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 50 to 150	$^{\circ}\text{C}$
Soldering Recommendations (Peak Temperature) <sup>c, d</sup>		260	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

b.  $t = 10\text{ s}$ .

c. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.

d. Package limited.

e. Based on  $T_C = 25\text{ }^{\circ}\text{C}$

**THERMAL RESISTANCE RATINGS**

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a, b</sup>	$t \leq 10 \text{ s}$	$R_{thJA}$	26	33	°C/W
Maximum Junction-to-Case (Drain)	Steady State	$R_{thJC}$	1.9	2.4	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

b. Maximum under Steady State conditions is 81 °C/W.

**SPECIFICATIONS** ( $T_J = 25 \text{ }^{\circ}\text{C}$ , unless otherwise noted)

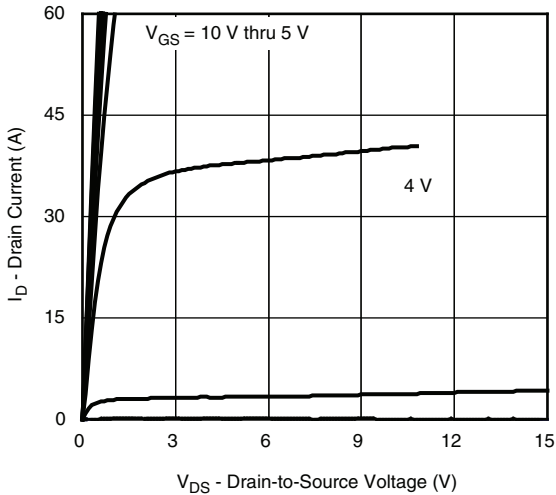
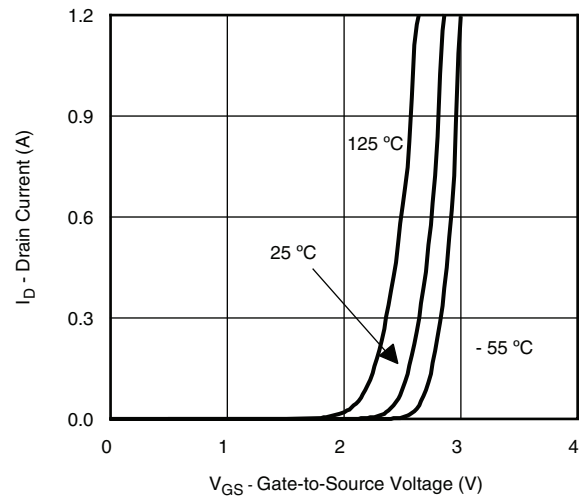
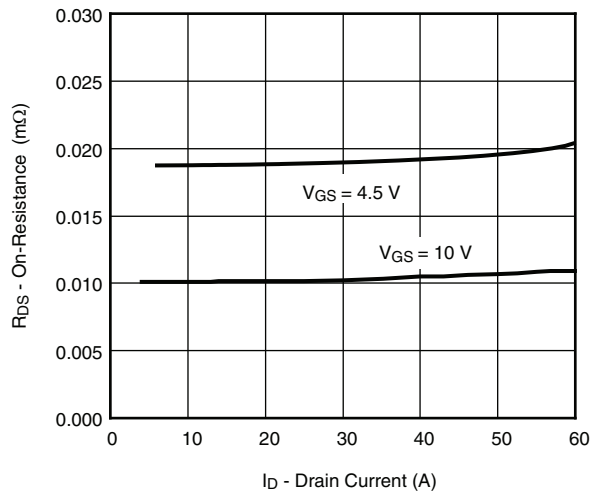
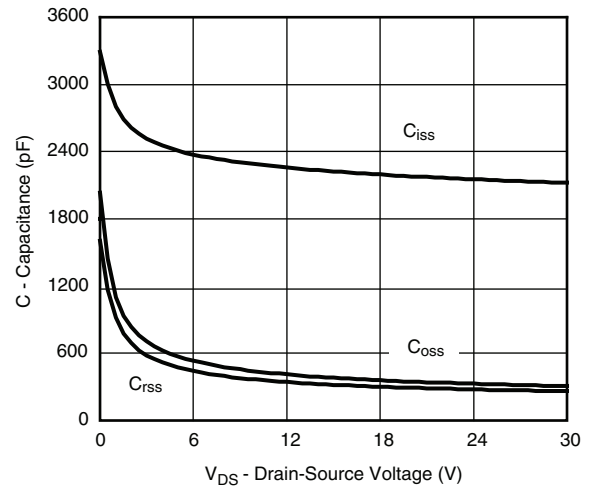
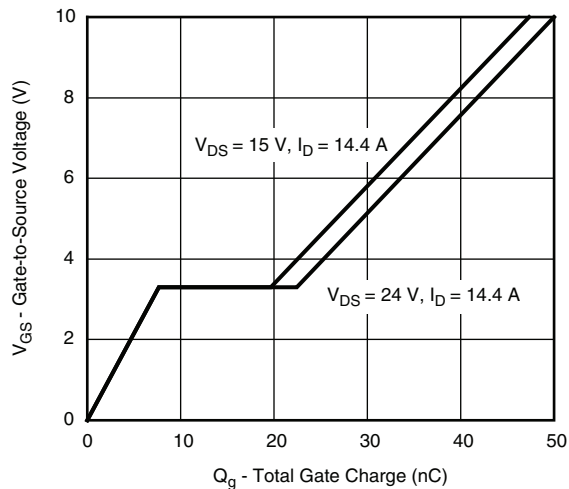
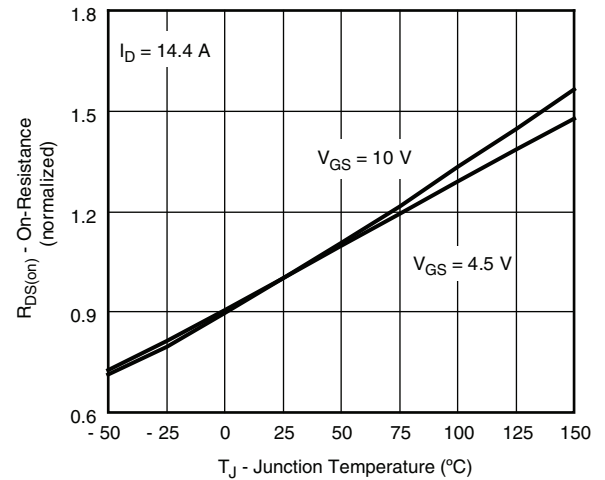
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = - 250 μA	- 30			V
V <sub>DS</sub> Temperature Coefficient	ΔV <sub>DS</sub> /T <sub>J</sub>	I <sub>D</sub> = - 250 μA		- 20		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	ΔV <sub>GS(th)</sub> /T <sub>J</sub>			5		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = - 250 μA	- 1.5		- 2.8	V
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V			- 1	μA
		V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≤ - 5 V, V <sub>GS</sub> = - 10 V	- 20			A
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 14.4 A		11		mΩ
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 11.5 A		18		
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 14.4 A		37		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		2000		pF
Output Capacitance	C <sub>oss</sub>			385		
Reverse Transfer Capacitance	C <sub>rss</sub>			322		
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 14.4 A			15	nC
					14	
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 14.4 A			7	
Gate-Drain Charge	Q <sub>gd</sub>				9	
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.4	1.8	3.6	Ω
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = - 15 V, R <sub>L</sub> = 1.5 Ω I <sub>D</sub> ≡ - 10 A, V <sub>GEN</sub> = - 4.5 V, R <sub>g</sub> = 1 Ω		50	75	ns
Rise Time	t <sub>r</sub>			43	65	
Turn-Off DelayTime	t <sub>d(off)</sub>			30	45	
Fall Time	t <sub>f</sub>			14	21	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = - 15 V, R <sub>L</sub> = 1.5 Ω I <sub>D</sub> ≡ - 10 A, V <sub>GEN</sub> = - 10 V, R <sub>g</sub> = 1 Ω		14	21	
Rise Time	t <sub>r</sub>			9	18	
Turn-Off DelayTime	t <sub>d(off)</sub>			36	54	
Fall Time	t <sub>f</sub>			10	20	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 35 <sup>e</sup>	A
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				- 60	
Body Diode Voltage	V <sub>SD</sub>	I <sub>F</sub> = - 10 A		- 0.8	- 1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 10 A, dI/dt = 100 A/μs, T <sub>J</sub> = 25 °C		31	47	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			30	45	nC
Reverse Recovery Fall Time	t <sub>a</sub>			15		ns
Reverse Recovery Rise Time	t <sub>b</sub>			16		

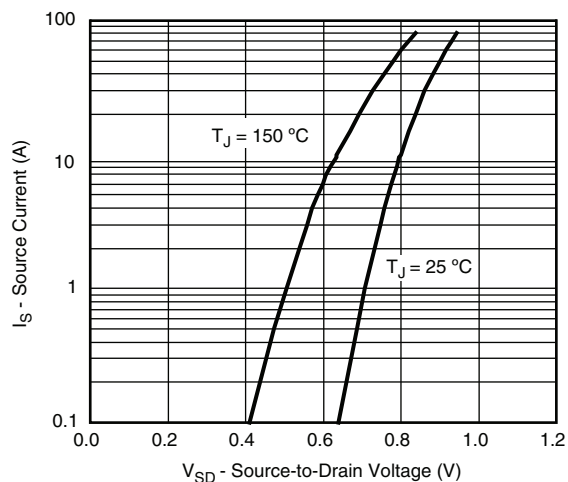
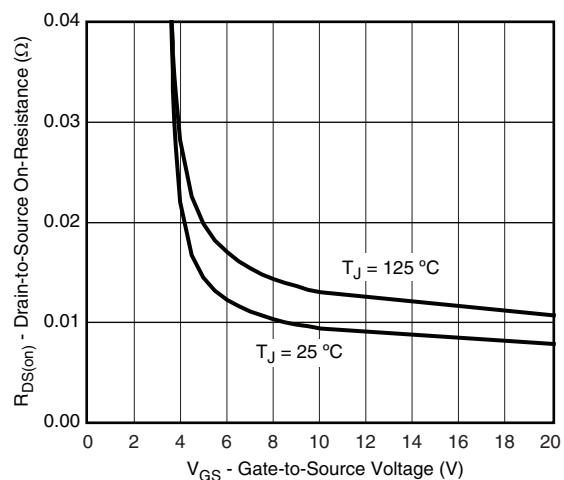
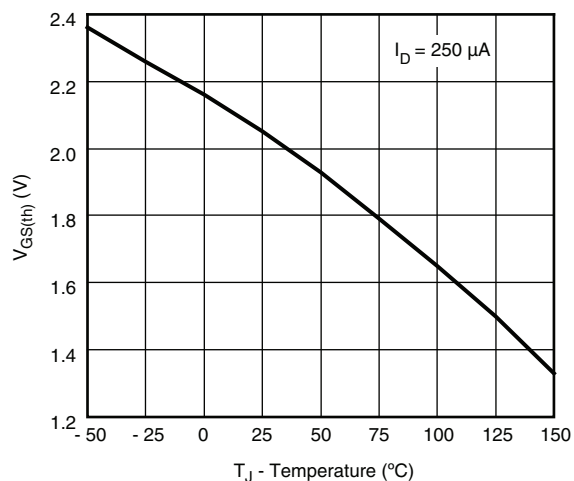
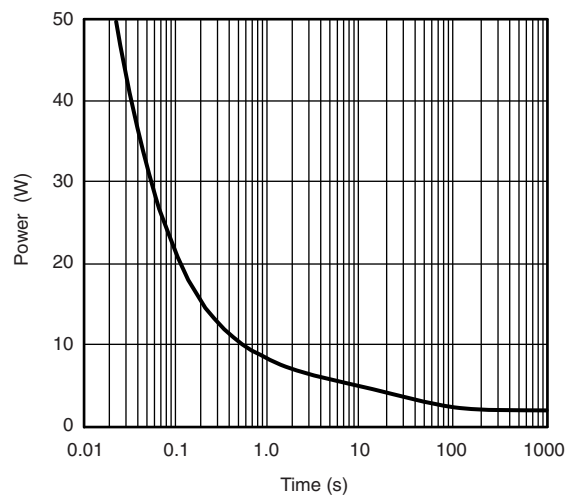
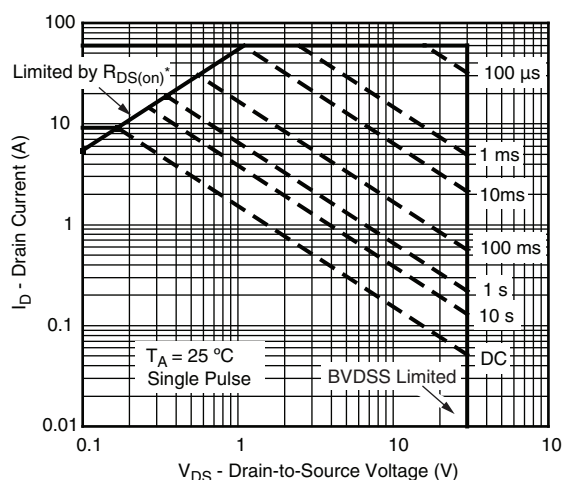
Notes:

a. Pulse test; pulse width  $\leq 300 \mu\text{s}$ , duty cycle  $\leq 2 \%$ .

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

**Output Characteristics**

**Transfer Characteristics**

**On-Resistance vs. Drain Current and Gate Voltage**

**Capacitance**

**Gate Charge**

**On-Resistance vs. Junction Temperature**

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

**Source-Drain Diode Forward Voltage**

**On-Resistance vs. Gate-to-Source Voltage**

**Threshold Voltage**

**Single Pulse Power, Junction-to-Ambient**


\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is Specified

**Safe Operating Area, Junction-to-Ambient**

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

 $T_C$  - Case Temperature (°C)

**Current Derating\***

 $T_C$  - Case Temperature (°C)

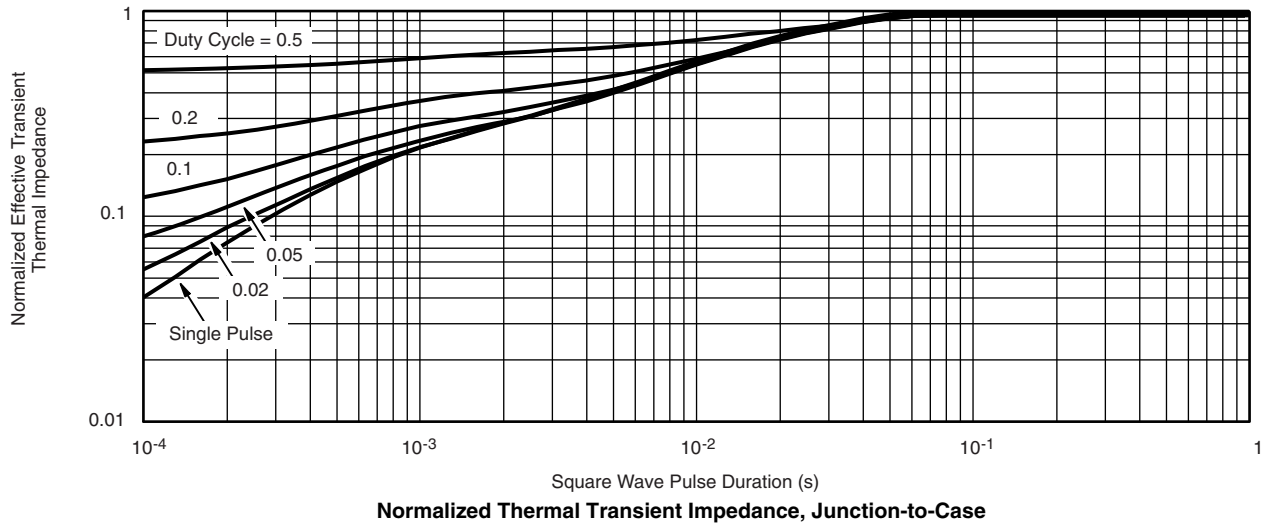
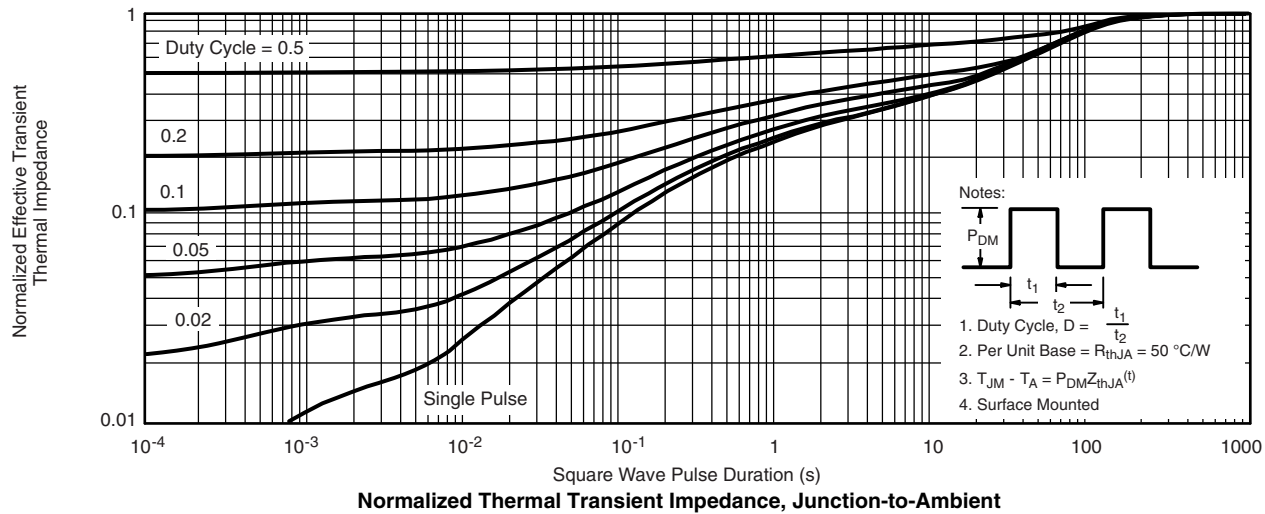
**Power, Junction-to-Case**

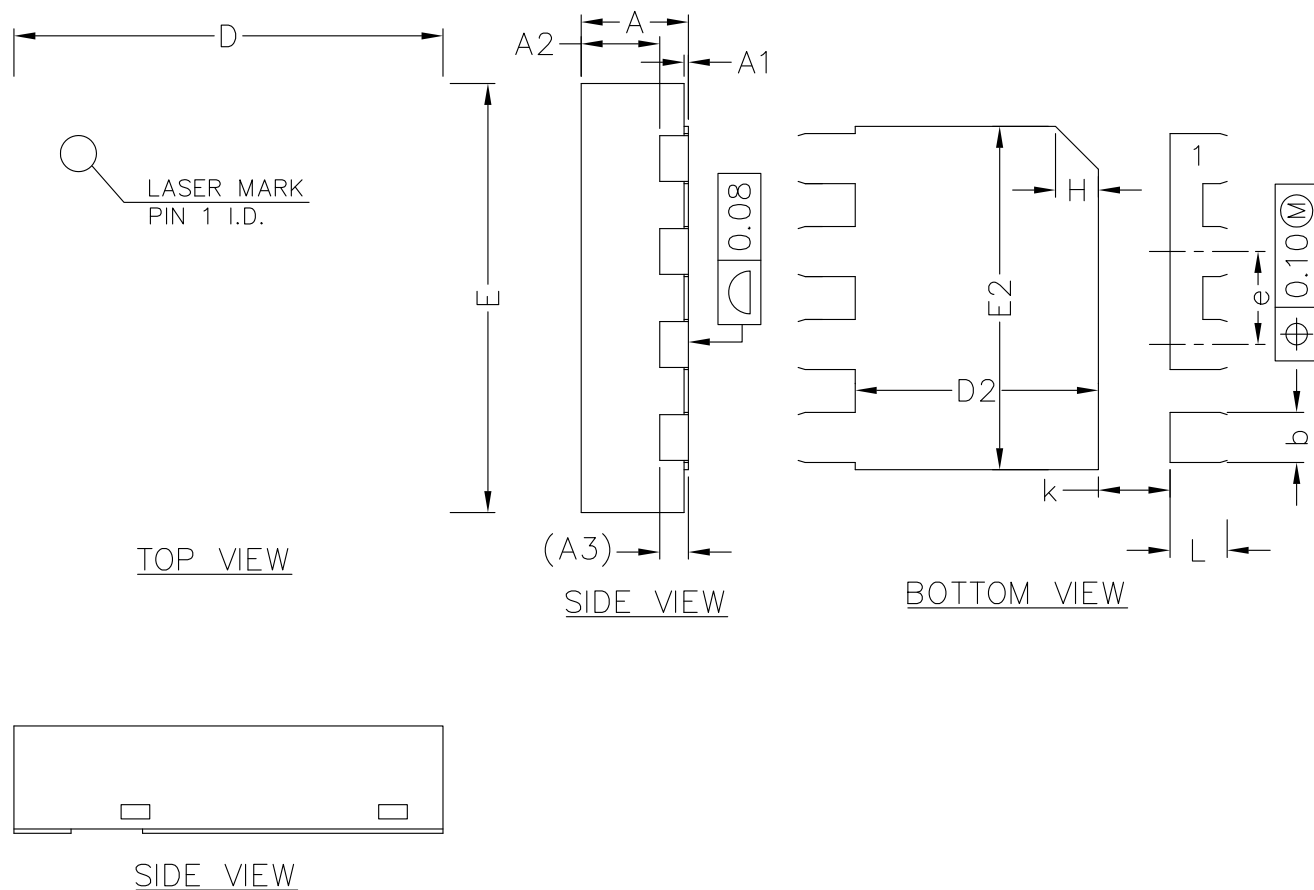
 $T_A$  - Ambient Temperature (°C)

**Power, Junction-to-Ambient**

\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)





COMMON DIMENSIONS  
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
A2	0.50	0.55	0.60
A3	0.20REF		
b	0.30	0.35	0.40
D	2.90	3.00	3.10
E	2.90	3.00	3.10
D2	1.60	1.70	1.80
E2	2.30	2.40	2.50
e	0.55	0.65	0.75
K	0.40	0.50	0.60
L	0.35	0.40	0.45

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