

RoHS

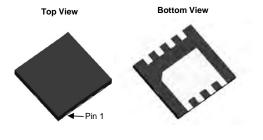
COMPLIANT HALOGEN **FREE**

NTTFS5811NLTAG-VB Datasheet

N-Channel 40 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A) ^f	Q _g (Тур.)		
40	0.0045 at V _{GS} = 10 V	40 ^g	9.8 nC		
40	0.0062 at V_{GS} = 4.5 V	40 ^g	3.0110		

DFN 3x3 EP



FEATURES

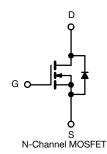
- Trench Power MOSFET
- 100 % R_q and UIS Tested
- Capable of Operating with 5 V Gate Drive

APPLICATIONS

- Synchronous Rectification
- Synchronous Buck Converters
- ORing
- Load Switching
- Motor Drive Switch

Top View





ABSOLUTE MAXIMUM RATIN	IGS (T _A = 25 °C	, unless otherv	wise noted)		
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V _{DS}	40	V	
Gate-Source Voltage		V _{GS}	± 20		
	T _C = 25 °C		40 ^g		
Continuous Drain Current (T. 150 °C)	T _C = 70 °C		40 ^g		
Continuous Drain Current ($T_J = 150 \ ^{\circ}C$)	T _A = 25 °C	I _D	19.3 ^{a, b}		
	T _A = 70 °C		15.5 ^{a, b}	•	
Pulsed Drain Current (t = 100 µs)		I _{DM}	100	A	
Continuous Source-Drain Diode Current	T _C = 25 °C	1	40 ^g		
	T _A = 25 °C	- I _S	3.1 ^{a, b}		
Single Pulse Avalanche Current L = 0.1 mH Single Pulse Avalanche Energy L = 0.1 mH		I _{AS}	20		
		E _{AS}	20	mJ	
	T _C = 25 °C		52		
Maximum Dawar Dissinction	T _C = 70 °C		33	w	
Maximum Power Dissipation	T _A = 25 °C	P _D	3.7 ^{a, b}	vv	
	T _A = 70 °C	1 –	2.4 ^{a, b}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		
Soldering Recommendations (Peak Temperature) ^{c, d}			260		

THEDMAL DESIGTANCE DATINGS

INERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{a, e}	t ≤ 10 s	R _{thJA}	26	33	°C/W		
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	1.9	2.4	0/11		

Notes:

a. Surface mounted on 1" x 1" FR4 board.

b. t = 10 s. c. The DFN 3 x 3 EP is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.

d. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.
e. Maximum under steady state conditions is 81 °C/W.

f. Based on $T_C = 25$ °C. g. Package limited.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	L						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	40			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	1 050 - 4		56		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 6			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1.1		2.2	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zana Osta Malla da Dusia Osmanl		$V_{DS} = 40 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μΑ	
Zero Gate Voltage Drain Current	DSS	$V_{DS} = 40 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 ^{\circ}\text{C}$			10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5$ V, $V_{GS} = 10$ V	20			Α	
	, , ,	V _{GS} = 10 V, I _D = 20 A		0.0045			
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 10 A				Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 20 A		65		S	
Dynamic ^b				<u> </u>			
Input Capacitance	C _{iss}			1330		pF	
Output Capacitance	C _{oss}	V _{DS} = 20 V, V _{GS} = 0 V, f = 1 MHz		1200			
Reverse Transfer Capacitance	C _{rss}			66			
	Q _g -	$V_{DS} = 20 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ A}$		21.3 3		-	
Total Gate Charge				9.8	15	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = 20$ V, $V_{GS} = 4.5$ V, $I_{D} = 10$ A		3.2			
Gate-Drain Charge	Q _{gd}			2.5			
Output Charge	Q _{oss}	$V_{DS} = 20 V, V_{GS} = 0 V$		32	48		
Gate Resistance	R _g	f = 1 MHz	0.2	0.9	1.5	Ω	
Turn-On Delay Time	t _{d(on)}			22	44		
Rise Time	t _r	$V_{DD} = 20 V, R_1 = 2 \Omega$		65	120	-	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 10 \text{ Å}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		24	45		
Fall Time	t _f			9	18		
Turn-On Delay Time	t _{d(on)}			11	22	ns	
Rise Time	t _r	$V_{DD} = 20 \text{ V}, \text{ R}_{\text{I}} = 2 \Omega$		11	22	-	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 10$ Å, $V_{GEN} = 10$ V, $R_g = 1$ Ω		22	44		
Fall Time	t _f			9	18		
Drain-Source Body Diode Characteristic				<u> </u>			
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			40		
Pulse Diode Forward Current (t = 100 μ s)	I _{SM}	-			100	A	
Body Diode Voltage	V _{SD}	I _S = 4 A, V _{GS} = 0 V		0.75	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			31	60	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			17	34	nC	
Reverse Recovery Fall Time	t _a	$I_F = 10 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^\circ\text{C}$		13			
Reverse Recovery Rise Time	t _b	1		18		ns	

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

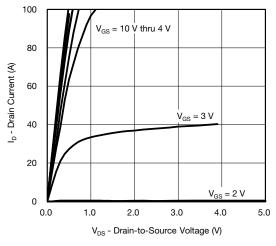
emi

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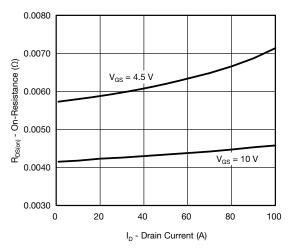
NTTFS5811NLTAG-VB



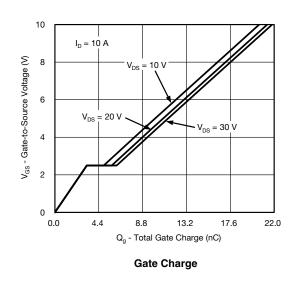
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

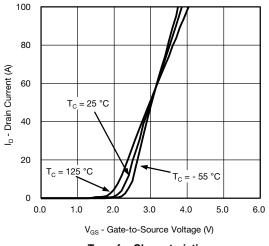




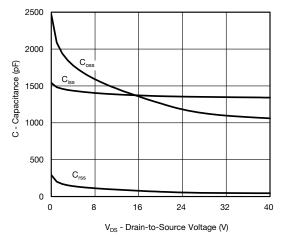


On-Resistance vs. Drain Current and Gate Voltage

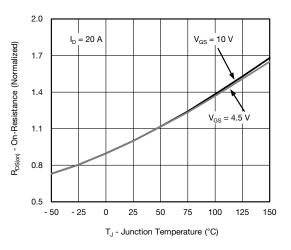




Transfer Characteristics



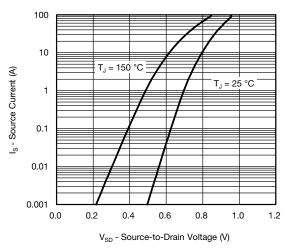
Capacitance

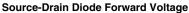


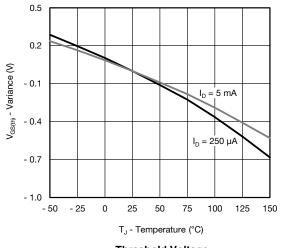
On-Resistance vs. Junction Temperature



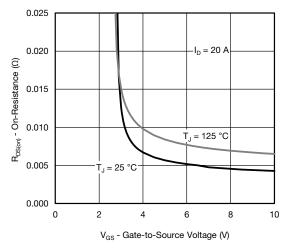
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



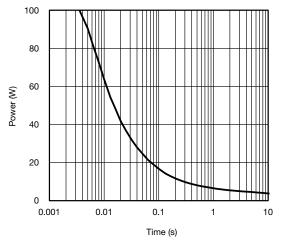




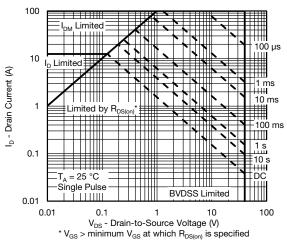
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



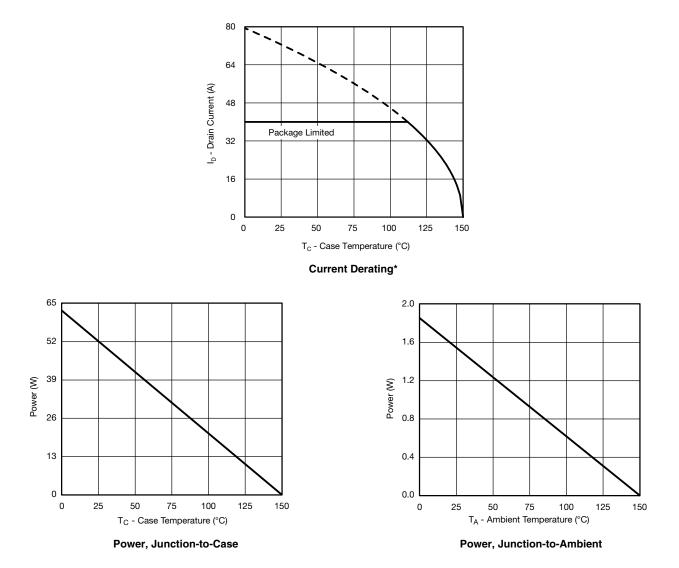
Single Pulse Power, Junction-to-Ambient



Safe Operating Area, Junction-to-Ambient



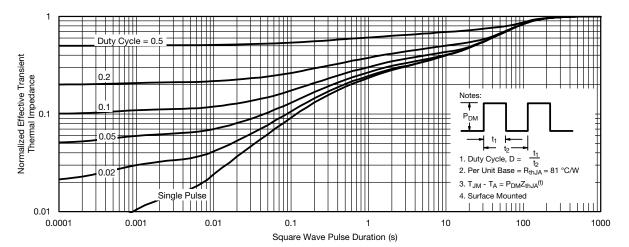
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



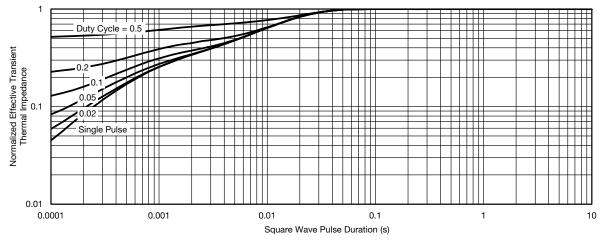
* The power dissipation P_D is based on $T_{J(max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

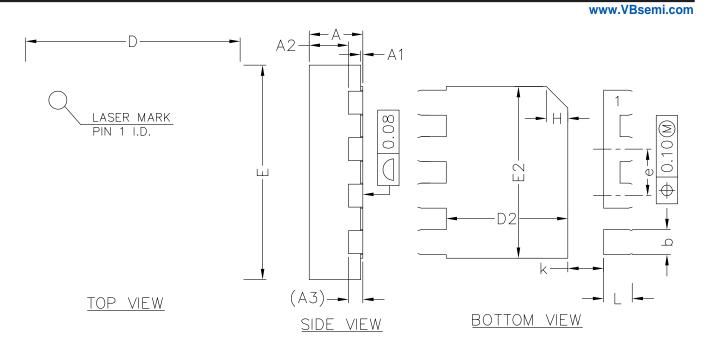


Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

NTTFS5811NLTAG-VB





<u>SIDE VIEW</u>

SYMBOL	MIN	NOM	MAX		
А	0.70	0.75	0.80		
A1	0.00	0.02	0.05		
A2	0.50	0.55	0.60		
A3	0.20REF				
b	0.30	0.35	0.40		
D	2.90	3.00	3.10		
E	2.90	3.00	3.10		
D2	1.60	1.70	1.80		
E2	2.30	2.40	2.50		
е	0.55	0.65	0.75		
K	0.40	0.50	0.60		
L	0.35	0.40	0.45		

COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

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