

# N7444-VB Datasheet

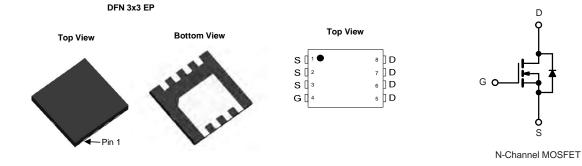
## N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>		
60	0.010 at V <sub>GS</sub> = 10 V	15		
00	0.013 at V <sub>GS</sub> = 4.5 V	12		

#### FEATURES

- 175 °C Junction Temperature
- Trench Power MOSFET
- Material categorization:





ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C, unless otherwise noted)						
Parameter		Symbol	Limit	Unit		
Gate-Source Voltage		V <sub>GS</sub>	± 20	V		
Continuous Ducia Current (T. 475 °C)b	T <sub>C</sub> = 25 °C	L.	15			
Continuous Drain Current (T <sub>J</sub> = 175 °C) <sup>b</sup>	T <sub>C</sub> = 100 °C	I <sub>D</sub>	13 <sup>a</sup>			
Pulsed Drain Current	I <sub>DM</sub>	100	А			
Continuous Source Current (Diode Conduction)	۱ <sub>S</sub>	50 <sup>a</sup>				
Avalanche Current	I <sub>AS</sub>	50				
Single Avalanche Energy (Duty Cycle $\leq$ 1 %)	L = 0.1 mH	E <sub>AS</sub>	125	mJ		
Maximum Dawar Dissinction	T <sub>C</sub> = 25 °C	P <sub>D</sub>	136	w		
Maximum Power Dissipation	T <sub>A</sub> = 25 °C		3 <sup>b</sup> , 8.3 <sup>b, c</sup>	VV		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 10 sec	R <sub>thJA</sub>	15	18	°C/W
Maximum Junction-to-Ambient*	Steady State		40	50	
Maximum Junction-to-Case		R <sub>thJC</sub>	0.85	1.1	
Notes:					

a. Package limited.

b. Surface mounted on 1" x 1" FR4 board.

c.  $t \le 10$  s.



SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)   Parameter Symbol Test Conditions Min. Typ. <sup>a</sup> Max.						L Init	
	Symbol	Test Conditions	st Conditions Min. T		wax.	Unit	
Static	N						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V$ , $I_D = 250 \mu A$	60			V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	1	2	3		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 \text{ °C}$			50	μA	
		$V_{DS} = 60$ V, $V_{GS} = 0$ V, $T_{J} = 175$ °C			250		
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 V, V_{GS} = 10 V$	60			Α	
		$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 20 \text{ A}$		0.010			
	P	$V_{GS}$ = 10 V, $I_{D}$ = 20 A, $T_{J}$ = 125 °C		0.016		Ω	
Drain-Source On-State Resistance <sup>b</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 175 °C		0.020			
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 15 A		0.013			
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 20 A		60		S	
Dynamic				·	I		
Input Capacitance	C <sub>iss</sub>			2650			
Output Capacitance	C <sub>oss</sub>	$V_{GS}$ = 0 V, $V_{DS}$ = 25 V, f = 1 MHz		470		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			225		1	
Total Gate Charge <sup>c</sup>	Qg			47	70		
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS}$ = 30 V, $V_{GS}$ = 10 V, $I_{D}$ = 50 A		10		nC	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			12			
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			10	20		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 30 \text{ V}, \text{ R}_{1} = 0.6 \Omega$		15	25	ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 50 \text{ A}, \text{ V}_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{g}} = 2.5 \ \Omega$		35	50		
Fall Time <sup>c</sup>	t <sub>f</sub>			20	30		
Source-Drain Diode Ratings and Cha	aracteristics (	T <sub>C</sub> = 25 °C)			I		
Pulsed Current	I <sub>SM</sub>				60	А	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>F</sub> = 20 A, V <sub>GS</sub> = 0 V		1	1.5	V	
	05	I <sub>F</sub> = 20 A, di/dt = 100 A/μs					

Notes:

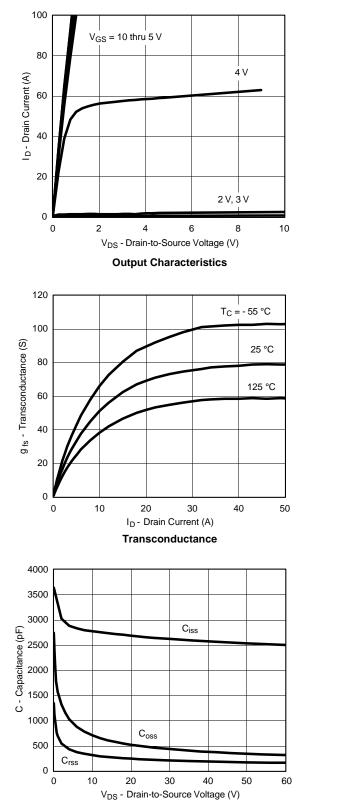
a. For design aid only; not subject to production testing.

b. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2$  %.

c. Independent of operating temperature.

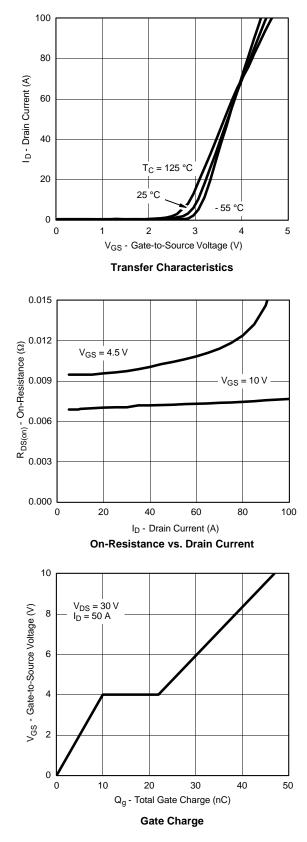
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



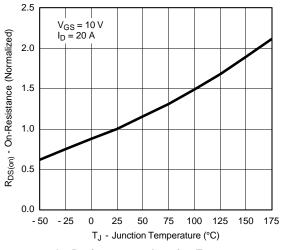


#### TYPICAL CHARACTERISTICS (25 °C unless noted)

Capacitance

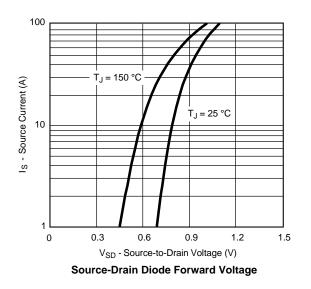






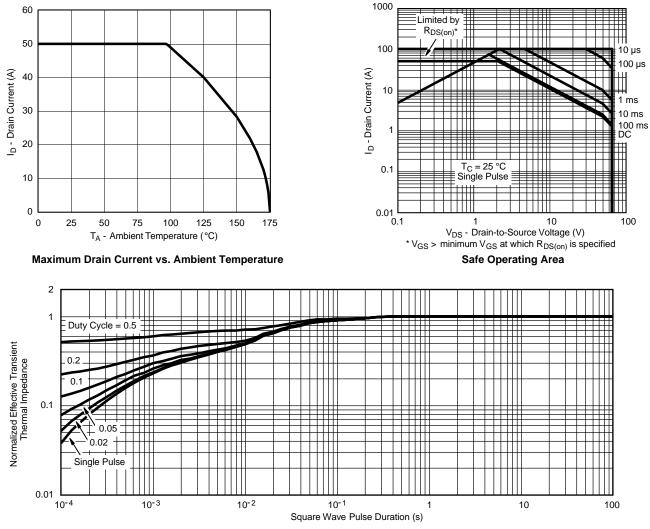
#### TYPICAL CHARACTERISTICS (25 °C unless noted)

**On-Resistance vs. Junction Temperature** 



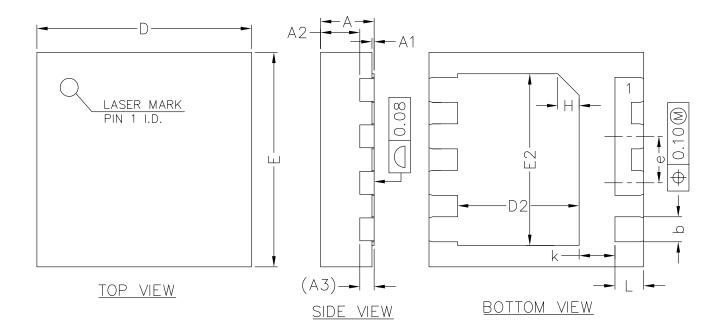


#### **THERMAL RATINGS**



Normalized Thermal Transient Impedance, Junction-to-Case







<u>SIDE VIEW</u>

(				
SYMBOL	MIN	NOM	МАХ	
А	0.70	0.75	0.80	
A1	0.00	0.02	0.05	
A2	0.50	0.55	0.60	
А3	0.20REF			
b	0.30	0.35	0.40	
D	2.90	3.00	3.10	
E	2.90	3.00	3.10	
D2	1.60	1.70	1.80	
E2	2.30	2.40	2.50	
е	0.55	0.65	0.75	
К	0.40	0.50	0.60	
L	0.35	0.40	0.45	

# COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)



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