

N7404-VB Datasheet

N-Channel 20 V (D-S) MOSFET



RoHS
COMPLIANT
HALOGEN
FREE

PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	Q _g (TYP.)
20	0.0055 at V _{GS} = 4.5V	58	9.4 nC
	0.0057 at V _{GS} = 2.5 V	45	

FEATURES

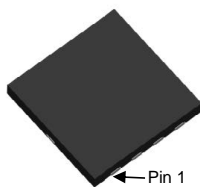
- Trench power MOSFET
- 100 % R_g and UIS tested

APPLICATIONS

- High power density DC/DC
- Synchronous rectification
- Embedded DC/DC

DFN 3x3 EP

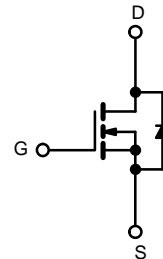
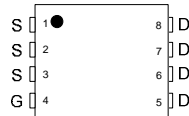
Top View



Bottom View



Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage		V _{GS}	+12	
Continuous Drain Current (T _J = 150 °C)	T _C = 25 °C	I _D	58	A
	T _C = 70 °C		46	
	T _A = 25 °C		19.8 ^{b, c}	
	T _A = 70 °C		15.8 ^{b, c}	
Pulsed Drain Current (t = 300 μs)		I _{DM}	150	
Continuous Source-Drain Diode Current	T _C = 25 °C	I _S	14.1	
	T _A = 25 °C		3.2 ^{b, c}	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	15	mJ
Single Pulse Avalanche Energy		E _{AS}	11.25	
Maximum Power Dissipation	T _C = 25 °C	P _D	31.2	W
	T _C = 70 °C		20	
	T _A = 25 °C		3.6 ^{b, c}	
	T _A = 70 °C		2.3 ^{b, c}	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C
Soldering Recommendations (Peak Temperature) ^{d, e}			260	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum Junction-to-Ambient ^{b, f}	t ≤ 10 s	R _{thJA}	24	34	°C/W
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	3	4	

Notes

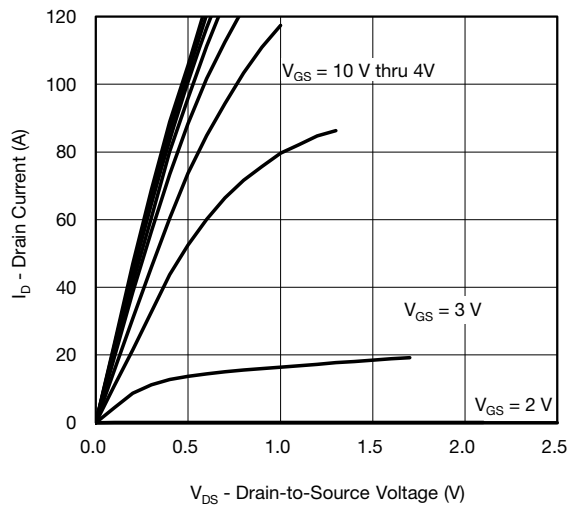
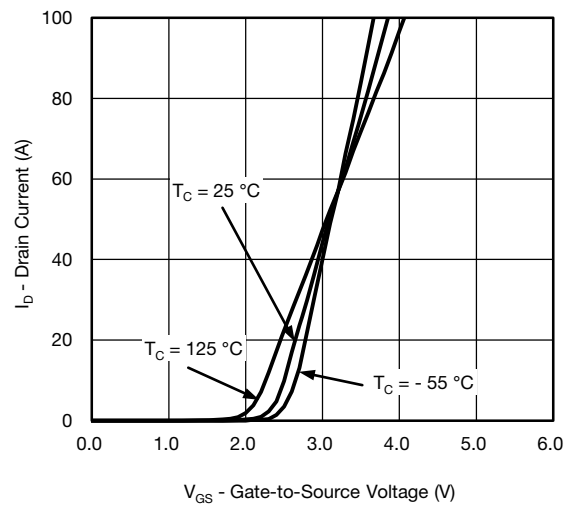
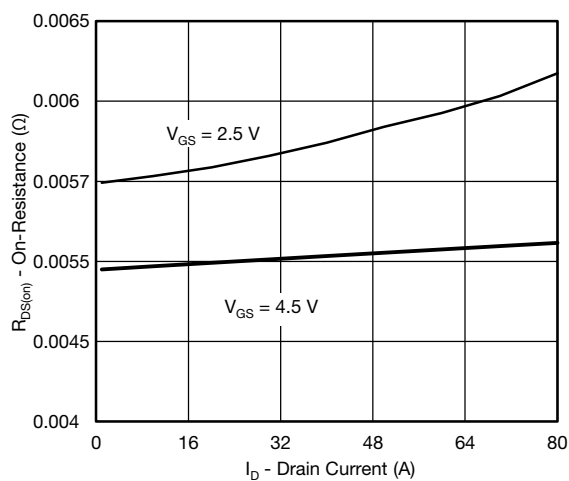
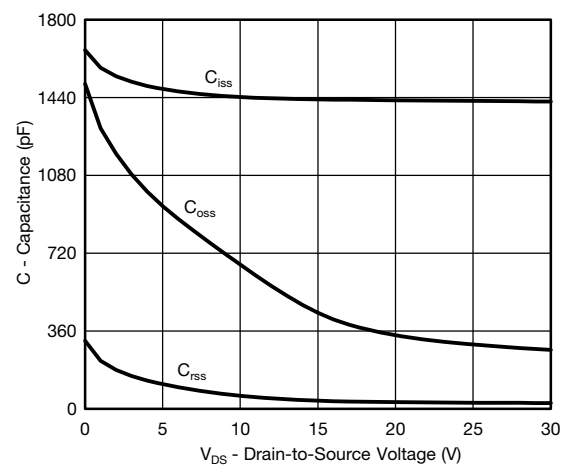
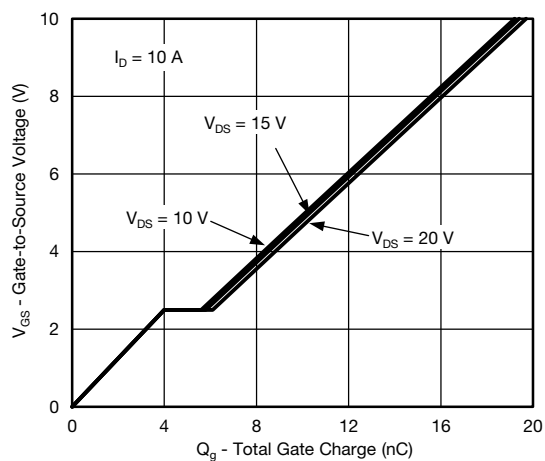
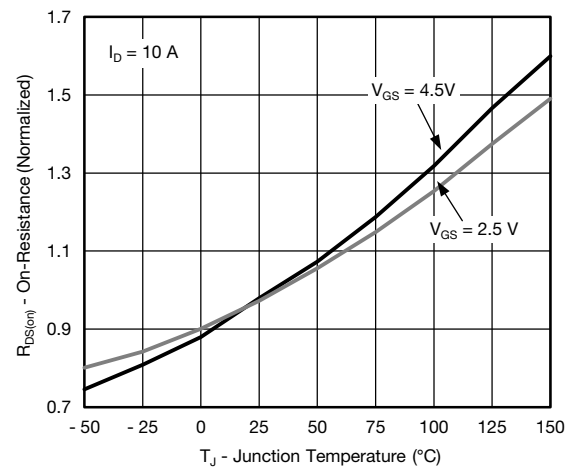
- Based on T_C = 25 °C.
- Surface mounted on 1" x 1" FR4 board.
- t = 10 s.
- The DFN3X3 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- Rework conditions: Manual soldering with a soldering iron is not recommended for leadless components.
- Maximum under steady state conditions is 70 °C/W.

SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	20	-	-	V
Drain-Source Breakdown Voltage (transient) ^c	V _{DS} t	V _{GS} = 0 V, I _{D(aval)} = 15 A, t _{transient} = 50 ns	26	-	-	
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA	-	20	-	mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J		-	-4.6	-	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	0.5	-	1.5	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = 12V	-	-	± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0 V	-	-	1	μA
		V _{DS} = 20 V, V _{GS} = 0 V, T _J = 55 °C	-	-	10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	30	-	-	A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 10 A	-	0.0055	-	Ω
		V _{GS} = 2.5 V, I _D = 8 A	-	0.0057	-	
Forward Transconductance ^a	g _{fs}	V _{DS} = 10 V, I _D = 10 A	-	65	-	S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz	-	1450	-	pF
Output Capacitance	C _{oss}		-	445	-	
Reverse Transfer Capacitance	C _{rss}		-	38	-	
C _{rss} /C _{iss} Ratio			-	0.026	0.052	
Total Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 10 A	-	19.4	29	nC
		V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 10 A	-	9.4	14	
Gate-Source Charge	Q _{gs}		-	4	-	
Gate-Drain Charge	Q _{gd}		-	1.8	-	
Output Charge	Q _{oss}	V _{DS} = 15 V, V _{GS} = 0 V	-	12.5	-	
Gate Resistance	R _g	f = 1 MHz	0.4	1.65	3.3	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 1.5 Ω I _D ≅ 10 A, V _{GEN} = 10 V, R _g = 1 Ω	-	9	18	ns
Rise Time	t _r		-	8	16	
Turn-Off Delay Time	t _{d(off)}		-	18	36	
Fall Time	t _f		-	8	16	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 1.5 Ω I _D ≅ 10 A, V _{GEN} = 4.5 V, R _g = 1 Ω	-	15	30	
Rise Time	t _r		-	12	24	
Turn-Off Delay Time	t _{d(off)}		-	18	36	
Fall Time	t _f		-	9	18	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	-	-	14.1	A
Pulse Diode Forward Current ^a	I _{SM}		-	-	80	
Body Diode Voltage	V _{SD}	I _S = 3 A	-	0.76	1.1	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 10 A, dI/dt = 100 A/μs, T _J = 25 °C	-	24	48	ns
Body Diode Reverse Recovery Charge	Q _{rr}		-	14	28	nC
Reverse Recovery Fall Time	t _a		-	12	-	ns
Reverse Recovery Rise Time	t _b		-	12	-	

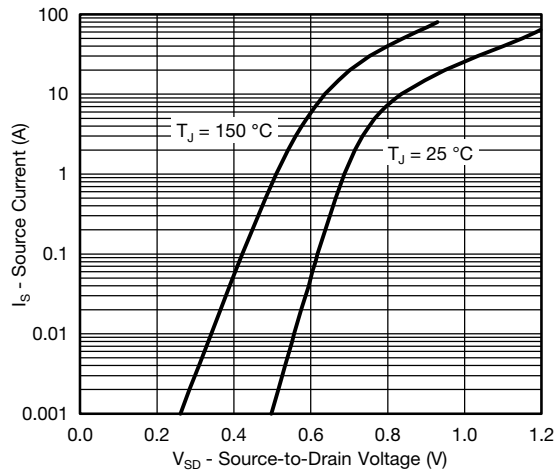
Notes

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.
 c. $T_{CASE} = 25\text{ }^{\circ}\text{C}$. Expected voltage stress during 100 % UIS test. Production datalog is not available.

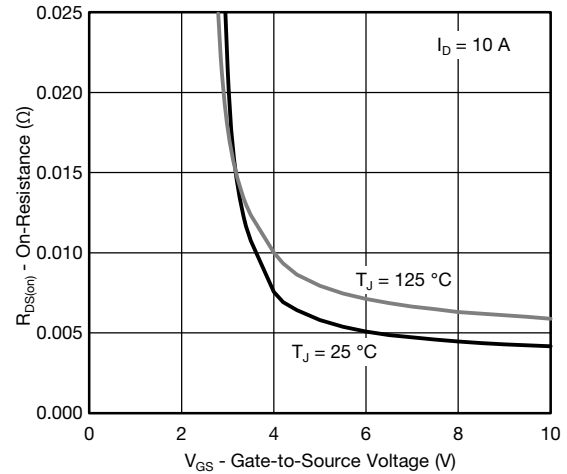
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Output Characteristics

Transfer Characteristics

On-Resistance vs. Drain Current

Capacitance

Gate Charge

On-Resistance vs. Junction Temperature

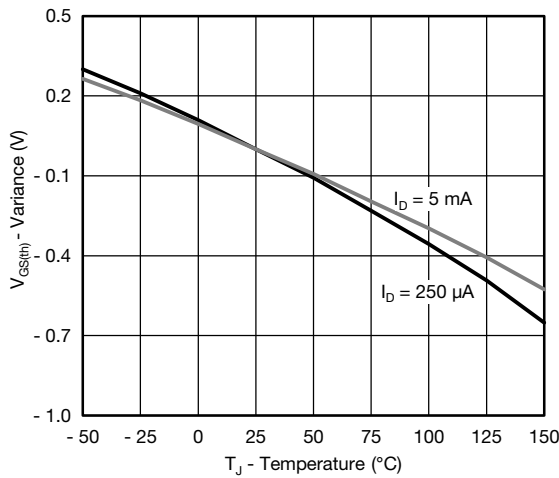
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



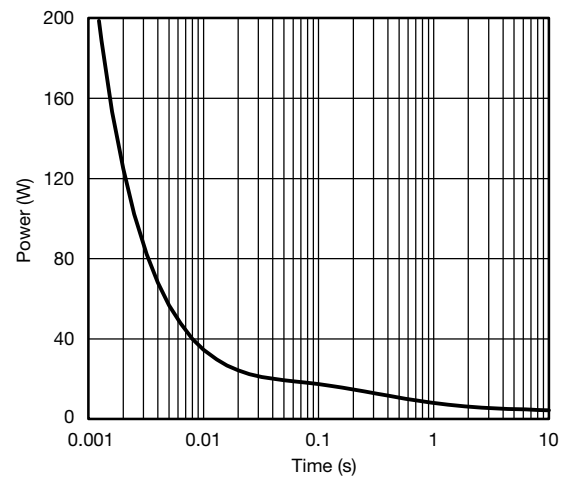
Source-Drain Diode Forward Voltage



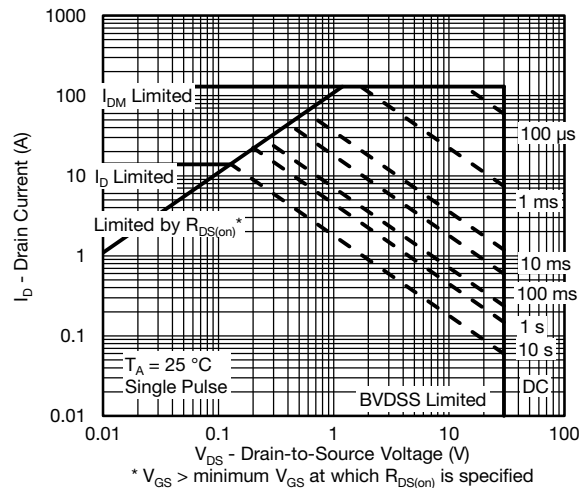
On-Resistance vs. Gate-to-Source Voltage



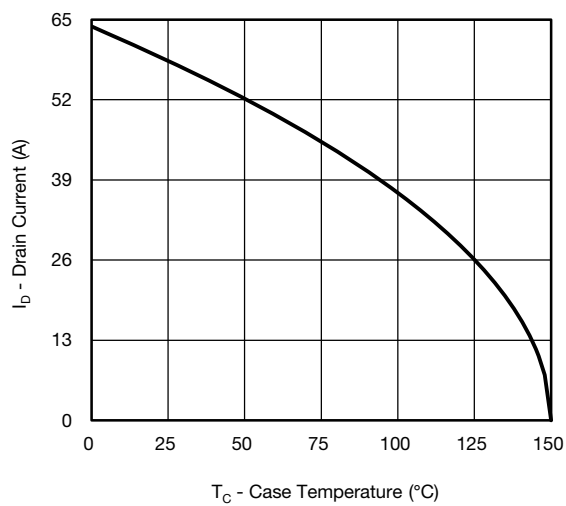
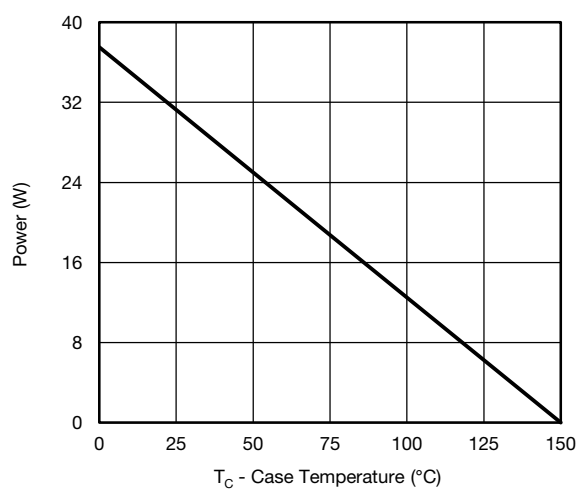
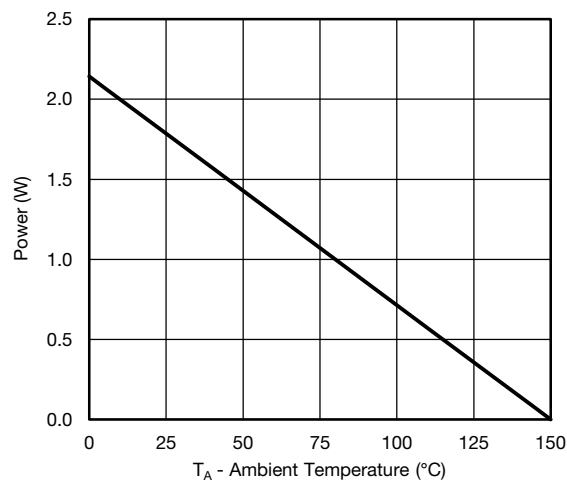
Threshold Voltage



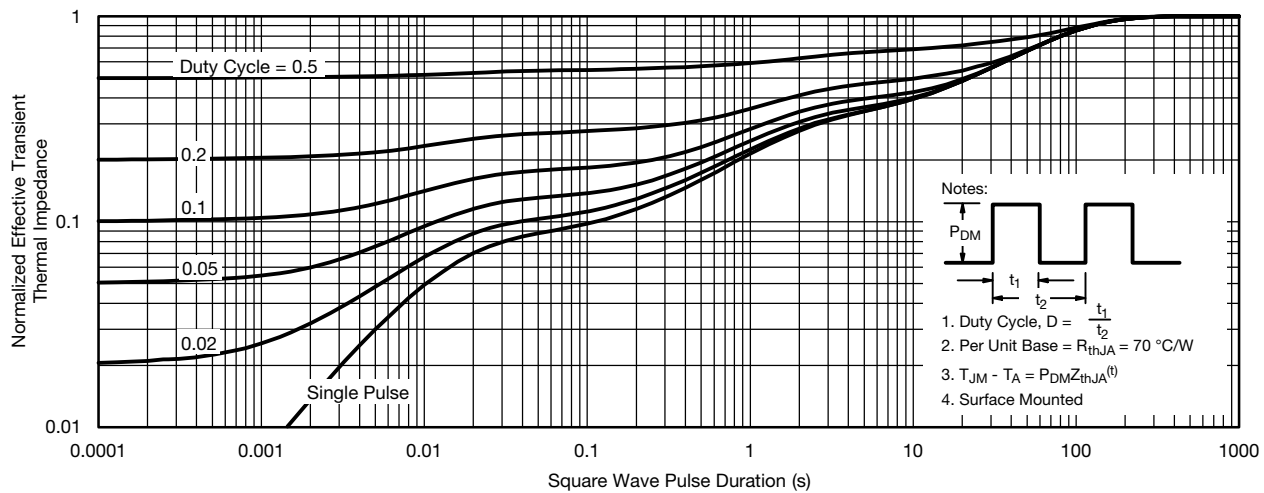
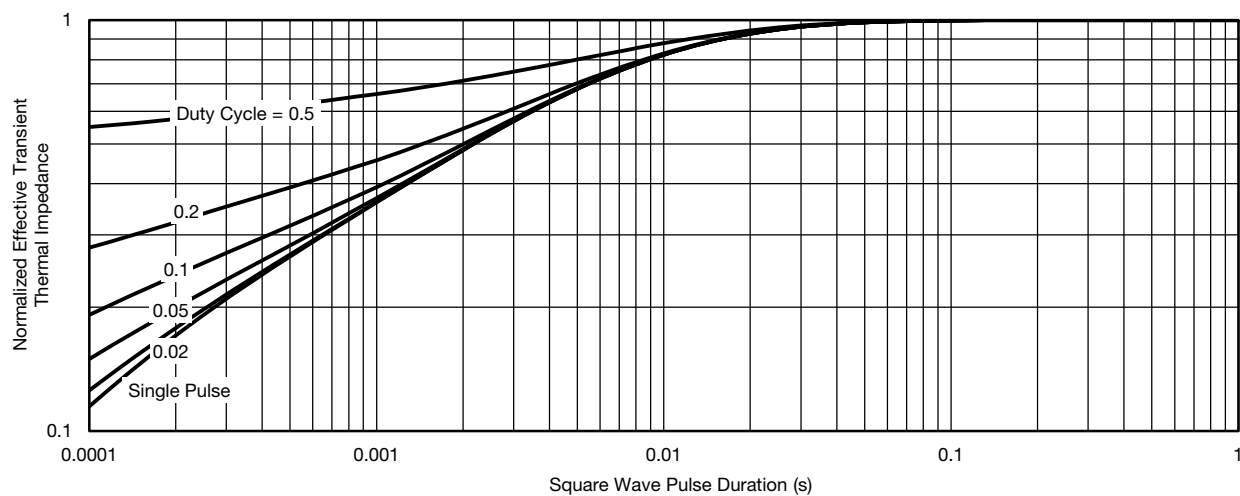
Single Pulse Power, Junction-to-Ambient

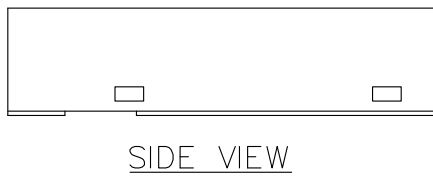
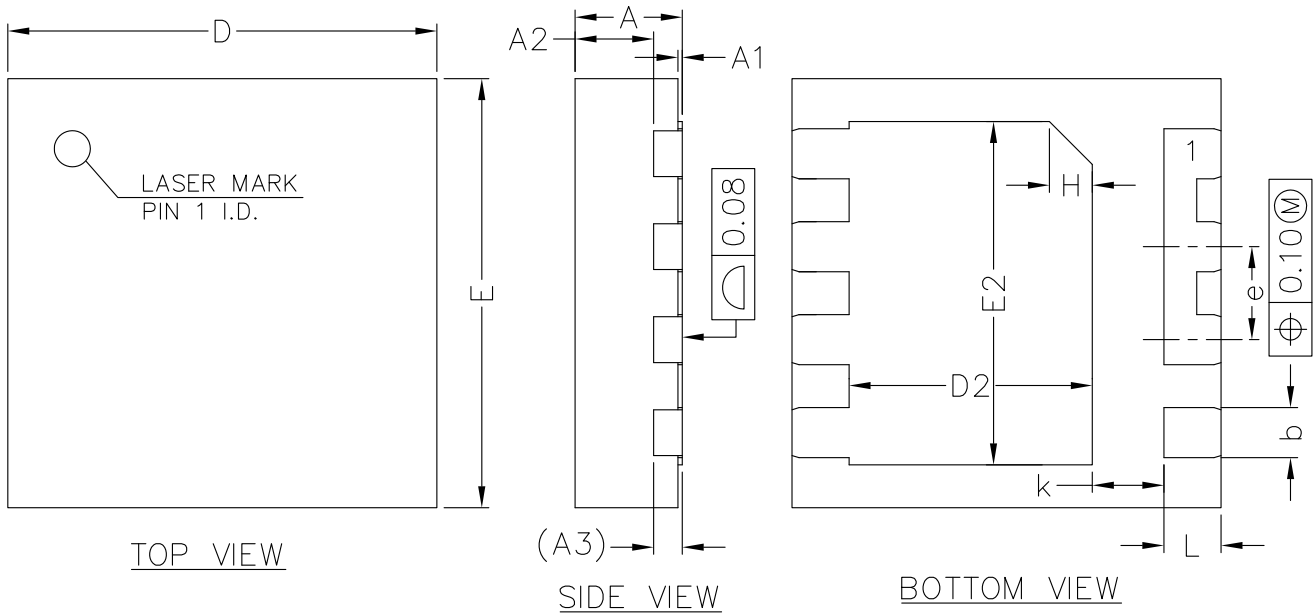


Safe Operating Area

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Current Derating*

Power, Junction-to-Case

Power, Junction-to-Ambient

* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Normalized Thermal Transient Impedance, Junction-to-Ambient

Normalized Thermal Transient Impedance, Junction-to-Case



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
A2	0.50	0.55	0.60
A3	0.20REF		
b	0.30	0.35	0.40
D	2.90	3.00	3.10
E	2.90	3.00	3.10
D2	1.60	1.70	1.80
E2	2.30	2.40	2.50
e	0.55	0.65	0.75
K	0.40	0.50	0.60
L	0.35	0.40	0.45

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