

**RoHS** 

HALOGEN FREE

# MI8435-VB Datasheet P-Channel 30 V (D-S) MOSFET

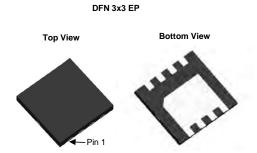
$V_{DS}$	-30	V	
$R_{DS(on),typ}$	V <sub>GS</sub> =10V	11	$m\Omega$
R <sub>DS(on),typ</sub>	V <sub>GS</sub> =4.5V	18	mΩ
ID	-45	Α	

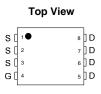
#### **FEATURES**

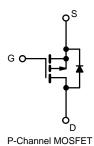
- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFET
- Low Thermal Resistance Power Package with Small Size and Low 1.07 mm Profile
- 100 % R<sub>g</sub> and UIS Tested
- · Compliant to RoHS Directive 2002/95/EC

### **APPLICATIONS**

- · Load Switch
- Adaptor Switch
- Notebook PC







Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V <sub>DS</sub>	- 30	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
	T <sub>C</sub> = 25 °C		- 45		
Continuous Proin Current /T = 150 °C)	T <sub>C</sub> = 70 °C		- 30		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 14.4 <sup>a, b</sup>		
	T <sub>A</sub> = 70 °C		- 11.5 <sup>a, b</sup>		
Pulsed Drain Current		I <sub>DM</sub>	- 60	A	
Osstinus of Ossue British Ossue	T <sub>C</sub> = 25 °C		- 35 <sup>e</sup>		
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	l <sub>S</sub>	- 3.2 <sup>a, b</sup>		
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	- 25		
Single-Pulse Avalanche Energy	L=0.11IIII	E <sub>AS</sub>	31.25	mJ	
	T <sub>C</sub> = 25 °C		52		
Mayimum Dawar Dissination	T <sub>C</sub> = 70 °C	P <sub>D</sub>	43	w	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	LD	3.8 <sup>a, b</sup>	VV	
	T <sub>A</sub> = 70 °C		2.4 <sup>a, b</sup>		
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 50 to 150	°C		
Soldering Recommendations (Peak Temperature) <sup>c, o</sup>		260			

#### Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.
- c.Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.
- d.Package limited.
- e.Based on T  $_{\rm C}$  = 25  $^{\circ}{\rm C}$



THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a, b</sup>	t ≤ 10 s	R <sub>thJA</sub>	26	33	°C/W
Maximum Junction-to-Case (Drain)	Steady State	R <sub>thJC</sub>	1.9	2.4	C/VV

#### Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. Maximum under Steady State conditions is 81 °C/W.

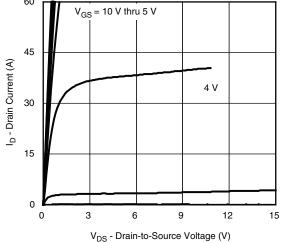
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static		<u>,                                      </u>					
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = - 250 μA		- 20		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	- Ι <sub>D</sub> = - 250 μΑ		5		mv/°C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.5		- 2.8	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zara Cata Valtara Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V		- 1			
Zero Gate Voltage Drain Current		V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 20			Α	
Durin Course Co Oleta Basistana a	B	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 14.4 A		11		m()	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 11.5 A		18		mΩ	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 14.4 A		37		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			2000		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		385			
Reverse Transfer Capacitance	C <sub>rss</sub>			322			
Total Gate Charge	$Q_g$	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -14.4 \text{ A}$			15		
					14		
Gate-Source Charge	$Q_gs$	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -14.4 \text{ A}$			7		
Gate-Drain Charge	$Q_{gd}$				9		
Gate Resistance	$R_{g}$	f = 1 MHz	0.4	1.8	3.6	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			50	75		
Rise Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, R_L = 1.5 \Omega$		43	65		
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong$ - 10 A, $V_{GEN}$ = - 4.5 V, $R_g$ = 1 $\Omega$		30	45		
Fall Time	t <sub>f</sub>			14	21	ns	
Turn-On Delay Time	t <sub>d(on)</sub>			14	21	113	
Rise Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, R_{L} = 1.5 \Omega$		9	18		
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong$ - 10 A, $V_{GEN}$ = - 10 V, $R_g$ = 1 $\Omega$		36	54		
Fall Time	t <sub>f</sub>			10	20		
<b>Drain-Source Body Diode Characterist</b>	ics						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 35 <sup>e</sup>	Α	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				- 60	] ^	
Body Diode Voltage	V <sub>SD</sub>	I <sub>F</sub> = - 10 A		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			31	47	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	- I <sub>F</sub> = - 10 A, dl/dt = 100 A/μs, T <sub>.J</sub> = 25 °C		30	45	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$\frac{1}{1}$ $\frac{1}$		15		ns	
Reverse Recovery Rise Time	t <sub>b</sub>	1		16			

#### Notes:

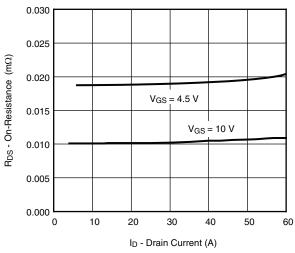
- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

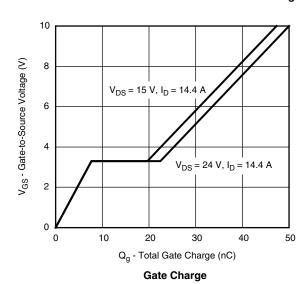




Output Characteristics



On-Resistance vs. Drain Current and Gate Voltage



1.2

(v) tueur 0.9

125 °C

25 °C

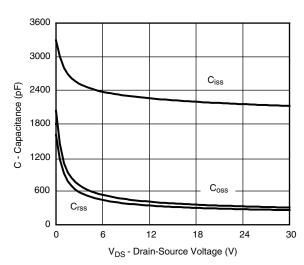
- 55 °C

0.0

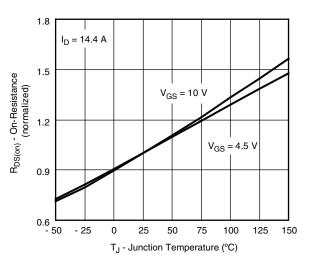
0 1 2 3 4

V<sub>GS</sub> - Gate-to-Source Voltage (V)

**Transfer Characteristics** 



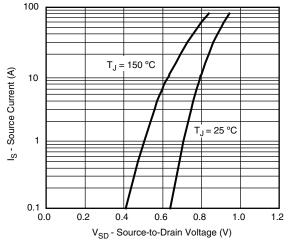
Capacitance



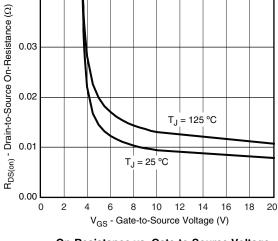
On-Resistance vs. Junction Temperature

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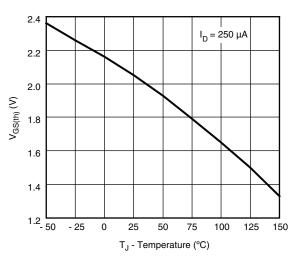




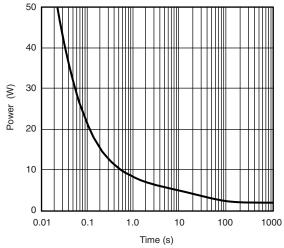




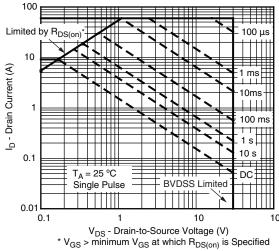
On-Resistance vs. Gate-to-Source Voltage



**Threshold Voltage** 



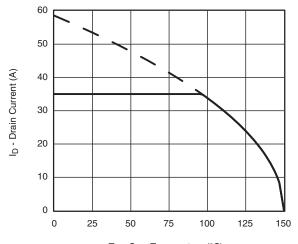
Single Pulse Power, Junction-to-Ambient



Safe Operating Area, Junction-to-Ambient

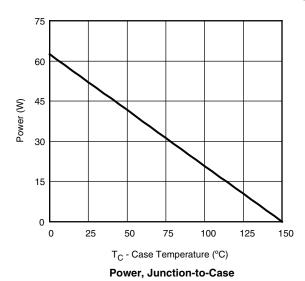
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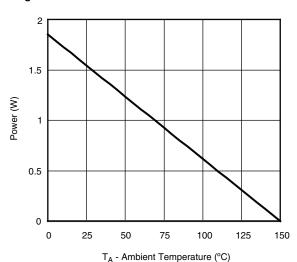




 $T_{\mbox{\scriptsize C}}$  - CaseTemperature (°C)

#### **Current Derating\***

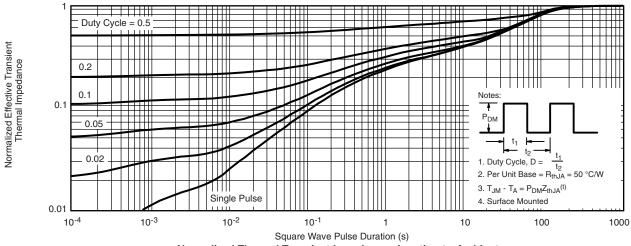




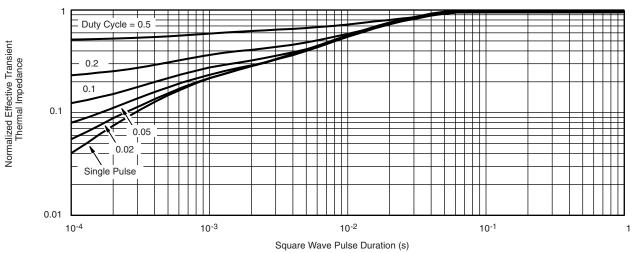
Power, Junction-to-Ambient

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit





Normalized Thermal Transient Impedance, Junction-to-Ambient

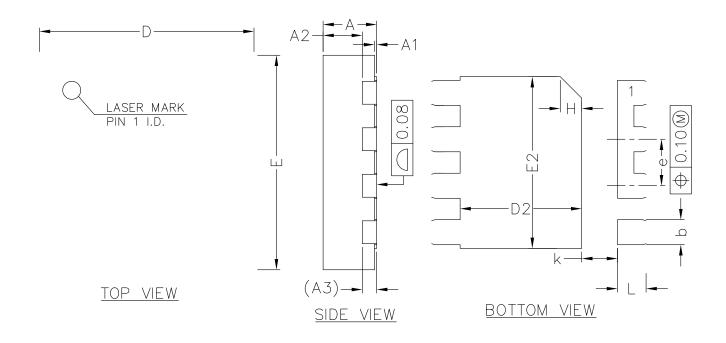


Normalized Thermal Transient Impedance, Junction-to-Case

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COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
А	0.70	0.75	0.80
A1	0.00	0.02	0.05
A2	0.50	0.55	0.60
А3		0.20REF	
Ь	0.30	0.35	0.40
D	2.90	3.00	3.10
Е	2.90	3.00	3.10
D2	1.60	1.70	1.80
E2	2.30	2.40	2.50
е	0.55	0.65	0.75
K	0.40	0.50	0.60
L	0.35	0.40	0.45



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