

FDMC7672-VB Datasheet N-Channel 30 V (D-S) MOSFET

Top View

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2

PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω) Typ.	I _D (A)	Q _g (Typ.)			
30	0.004 at V _{GS} = 4.5 V	60	33.5 nC			
30	0.005 at V_{GS} = 2.5 V	50	33.3110			

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFET
- ٠
- 100 % R_g and UIS Tested Compliant to RoHS Directive 2002/95/EC ٠

GC

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N-Channel MOSFET

APPLICATIONS

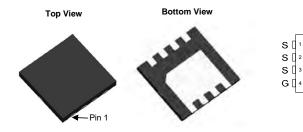
- Motor Control
- Industrial
- Load Switch
- ORing

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DFN 3x3 EP

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Drain Current (T _J = 150 °C)	$T_{C} = 25 °C$ $T_{C} = 70 °C$ $T_{A} = 25 °C$ $T_{A} = 70 °C$	I _D	60 ^{a, e} 40 ^{a, e} 22 ^{b, c} 15 ^{b, c}		
Pulsed Drain Current (t = 300 µs)		I _{DM}	150	A	
Continuous Source-Drain Diode Current	T _C = 25 °C T _A = 25 °C	I _S	35 3.3 ^{b, c}		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	20		
Single Pulse Avalanche Energy	L = 0.1 mm	E _{AS}	20	mJ	
Maximum Power Dissipation	$T_{C} = 25 °C$ $T_{C} = 70 °C$ $T_{A} = 25 °C$ $T_{A} = 70 °C$	P _D	52 33 3.7 ^{b, c} 2.4 ^{b, c}	W	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	J °	
Soldering Recommendations (Peak Temperature)		ž	260	-0	

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R _{thJA}	24	33	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	1.9	2.4	0/10	

Notes:

a. Based on $T_C = 25 \text{ °C}$. b. Surface mounted on 1" x 1" FR4 board.

c. t = 10 s.

d. Maximum under steady state conditions is 90 °C/W.

e. Calculated based on maximum junction temperature. Package limitation current is 80 A.



COMPLIANT



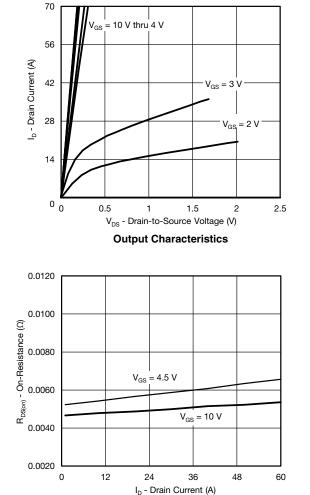
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static						1	
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L 050		30			
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	– I _D = 250 μΑ		- 5.6		mV/°	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	0.5		1.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			1	μA	
Zero Gate Voltage Drain Current	IDSS				10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5$ V, $V_{GS} = 10$ V	30			Α	
		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 10 \text{ A}$	0.0040				
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 7 \text{ A}$		0.0050		Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 10 A		65		S	
Dynamic ^b	<u> </u>					1	
Input Capacitance	C _{iss}			6000		pF	
Output Capacitance	C _{oss}	V_{DS} = 15 V, V_{GS} = 0 V, f = 1 MHz		406			
Reverse Transfer Capacitance	C _{rss}			360			
Total Gate Charge	Qg	$V_{DS} = 15 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ A}$		68	102	- nC	
				33.5	51		
Gate-Source Charge	Q _{gs}	V_{DS} = 15 V, V_{GS} = 4.5 V, I_{D} = 10 A		7.7			
Gate-Drain Charge	Q _{gd}			13.8			
Gate Resistance	Rg	f = 1 MHz	0.3	0.7	1.4	Ω	
Turn-On Delay Time	t _{d(on)}			24	45		
Rise Time	t _r	V_{DD} = 15 V, R_L = 1.5 Ω		24	45		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_{\text{D}}\cong$ 10 A, V_{GEN} = 4.5 V, R_{g} = 1 Ω		32	60		
Fall Time	t _f			12	24		
Turn-On Delay Time	t _{d(on)}			14	28	– ns – –	
Rise Time	t _r	V_{DD} = 15 V, R_L = 1.5 Ω		13	26		
Turn-Off Delay Time	t _{d(off)}	$I_{D}\cong$ 10 A, V_{GEN} = 10 V, R_{g} = 1 Ω		33	60		
Fall Time	t _f			8	16		
Drain-Source Body Diode Characteristi	cs						
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C		35		٨	
Pulse Diode Forward Current	I _{SM}			70		- A	
Body Diode Voltage	V _{SD}	$I_{S} = 3 A, V_{GS} = 0 V$		0.7	1.1	V	
Body Diode Reverse Recovery Time	t _{rr}			21	40	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	L = 10.4 dl/dt = 100.4/m T = 05.00		10	20	nC	
Reverse Recovery Fall Time	t _a	$I_F = 10 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^\circ\text{C}$		9			
Reverse Recovery Rise Time t _b		1		12		ns	

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 % b. Guaranteed by design, not subject to production testing.

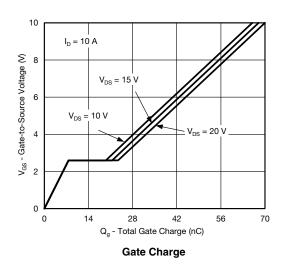
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

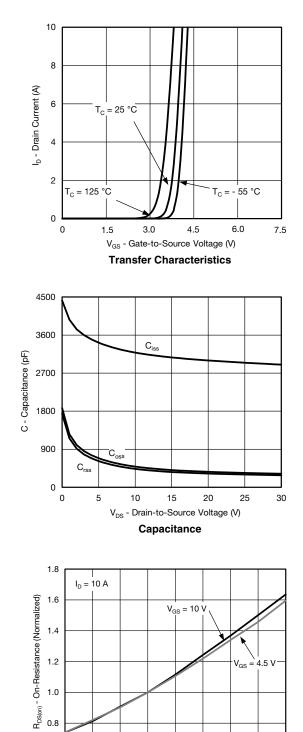




TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

On-Resistance vs. Drain Current and Gate Voltage



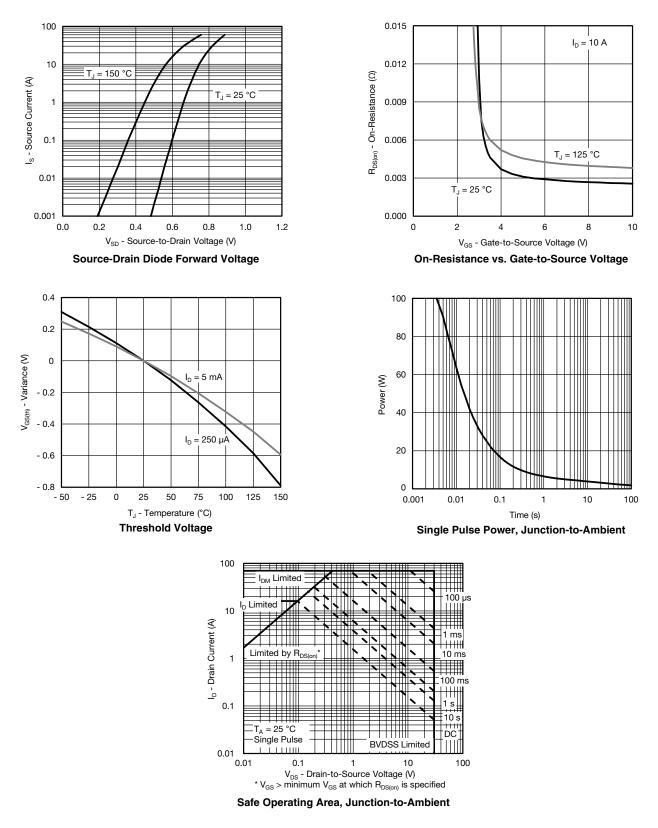


0.6 - 50 - 25 0 25 50 75 100 125 150 T_J - Junction Temperature (°C)

On-Resistance vs. Junction Temperature

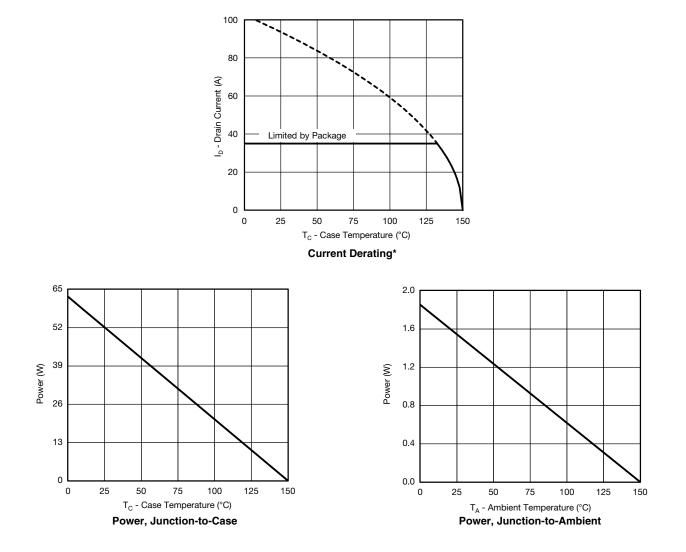








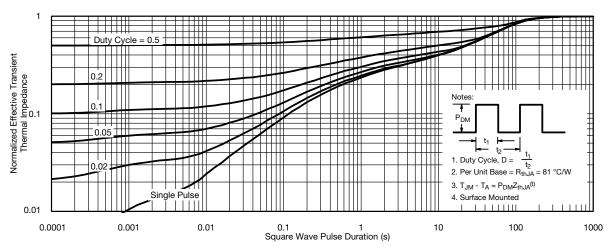
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



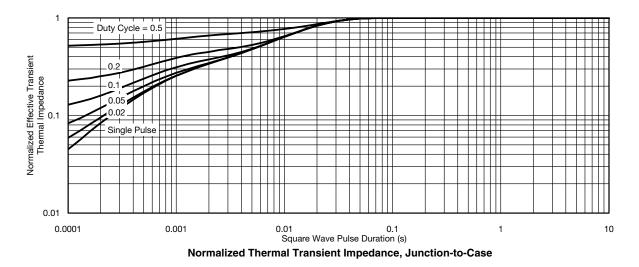
* The power dissipation P_D is based on $T_{J(max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



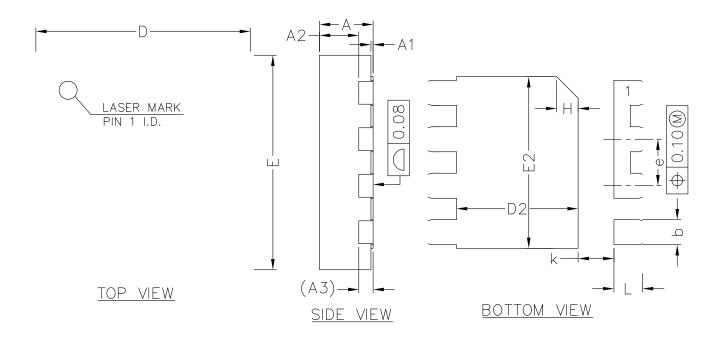
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)







FDMC7672-VB





<u>SIDE VIEW</u>

SYMBOL	MIN	NOM	МАХ		
А	0.70	0.75	0.80		
A1	0.00	0.02	0.05		
A2	0.50	0.55	0.60		
A3	0.20REF				
b	0.30	0.35	0.40		
D	2.90	3.00	3.10		
E	2.90	3.00	3.10		
D2	1.60	1.70	1.80		
E2	2.30	2.40	2.50		
е	0.55	0.65	0.75		
K	0.40	0.50	0.60		
	0.35	0.40	0.45		

COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

Bsemi

www.VBsemi.com



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