

FDMC2523P-VB Datasheet

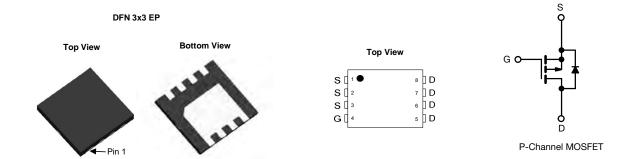
P-Channel 200V (D-S)MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	-200				
$R_{DS(on)}(\Omega)$	V _{GS} = -10 V	2.0			
Q _g max. (nC)	29				
Q _{gs} (nC)	5.4				
Q _{gd} (nC)	15				
Configuration	Single				

FEATURES

- Surface mount
- · Available in tape and reel
- Dynamic dV/dt rating
- Repetitive avalanche rated
- P-channel
- · Fast switching
- Ease of paralleling





PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-Source Voltage			V_{DS}	-200	.,,	
Gate-Source Voltage			V_{GS}	± 20	V	
Continuous Drain Current	V -+ 10.V	T _C = 25 °C T _C = 100 °C	- I _D	-3.6		
Continuous Drain Current	VGS at -10 V	T _C = 100 °C		-2.5	Α	
Pulsed Drain Current ^a			I _{DM}	-12		
Linear Derating Factor				0.59	W/°C	
Linear Derating Factor (PCB mount) e				0.025	WV/°C	
Single Pulse Avalanche Energy b			E _{AS}	500	mJ	
Avalanche Current ^a			I _{AR}	-6.4	А	
Repetitive Avalanche Energy a			E _{AR}	7.4	mJ	
Maximum Power Dissipation	T _C =	= 25 °C		74	W	
Maximum Power Dissipation (PCB mount) e	T _A =	25 °C	P_{D}	3.0	VV	
Peak Diode Recovery dV/dt ^c			dV/dt	-5.0	V/ns	
Operating Junction and Storage Temperature Range			T _J , T _{stg} -55 to +150	-55 to +150	***	
Soldering Recommendations (Peak temperature) d	for 10 s			300	°C	

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11). b. $V_{DD} = -50$ V, starting $T_J = 25$ °C, L = 17 mH, $R_g = 25$ Ω , $I_{AS} = -6.5$ A (see fig. 12). c. $I_{SD} \le -6.5$ A, $dI/dt \le 120$ A/µs, $V_{DD} \le V_{DS}$, $T_J \le 150$ °C. d. 1.6 mm from case. e. When mounted on 1" square PCB (FR-4 or G-10 material).



THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	TYP.	MAX.	UNIT		
Maximum Junction-to-Ambient	R _{thJA}	-	62			
Maximum Junction-to-Ambient (PCB mount) ^a	R _{thJA}	-	40	°C/W		
Maximum Junction-to-Case (Drain)	R_{thJC}	-	1.7			

Note

a. When mounted on 1" square PCB (FR-4 or G-10 material).

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static		•			L	L	
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0$, $I_D = -250 \mu\text{A}$		-200	-	-	V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Referenc	Reference to 25 °C, I _D = -1 mA		-0.24	-	V/°C
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$		-	-4.0	V
Gate-Source Leakage	I _{GSS}		V _{GS} = ± 20 V		-	± 10	μΑ
Zone Ooto Voltana Dusia Ormant	I _{DSS}	V _{DS} =	$V_{DS} = -200 \text{ V}, V_{GS} = 0 \text{ V}$		-	- 100	μΑ
Zero Gate Voltage Drain Current		V _{DS} = -160 V, V _{GS} = 0 V, T _J = 125 °C		-	-	-500	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = -10 V	I _D = -1.0 A ^b	-	2.00	-	Ω
Forward Transconductance	9 _{fs}	V _{DS} =	-50 V, I _D = -1.0 A ^b	2.8	-	-	S
Dynamic							
Input Capacitance	C _{iss}		$V_{GS} = 0 V$,		700	-	
Output Capacitance	C _{oss}		$V_{DS} = -25 V$,	-	200	-	pF
Reverse Transfer Capacitance	C _{rss}	f = 1	f = 1.0 MHz, see fig. 5		40	-	
Total Gate Charge	Qg			-	-	29	nC
Gate-Source Charge	Q_{gs}	V _{GS} = -10 V	$V_{GS} = -10 \text{ V}$ $I_{D} = -3.5 \text{A}, V_{DS} = -160 \text{ V},$ see fig. 6 and 13 b		-	5.4	
Gate-Drain Charge	Q _{gd}		occ lig. o and ro	-	-	15	1
Turn-On Delay Time	t _{d(on)}			-	12	-	
Rise Time	t _r	V_{DD} = -100 V, I_D = -3.5A, R_g = 12 Ω , R_D = 15 Ω , see fig. 10 $^{\rm b}$		-	27	-	ns
Turn-Off Delay Time	t _{d(off)}			-	28	-	
Fall Time	t _f				24	-	
Internal Drain Inductance	L _D	Between lead, 6 mm (0.25") from		-	4.5	-	- nH
Internal Source Inductance	L _S	package and die contact	package and center of die contact		7.5	-	
Gate Input Resistance	R _g	f = 1 MHz, open drain		0.6	-	3.7	Ω
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	MOSFET symbol showing the integral reverse p - n junction diode		-	-	-3 .5	
Pulsed Diode Forward Current ^a	I _{SM}			-	-	-6	A
Body Diode Voltage	V _{SD}	T _J = 25 °C	T _J = 25 °C, I _S = -3.5A, V _{GS} = 0 V ^b		-	-6.5	V
Body Diode Reverse Recovery Time	t _{rr}	-			200	300	ns
Body Diode Reverse Recovery Charge	Q _{rr}	$T_J = 25 ^{\circ}\text{C}$, $I_F = -3.5\text{A}$, $dI/dt = 100 \text{A/µs}^{-b}$		-	1.9	2.9	μC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by L _S and			L _D)		

Notes

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- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. Pulse width $\leq 300~\mu s;$ duty cycle $\leq 2~\%.$

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

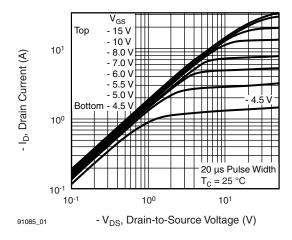


Fig. 1 - Typical Output Characteristics, T_C = 25 °C

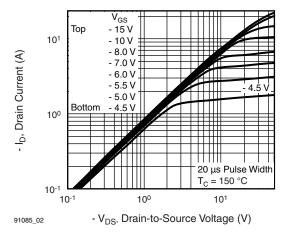


Fig. 2 - Typical Output Characteristics, $T_C = 150 \, ^{\circ}\text{C}$

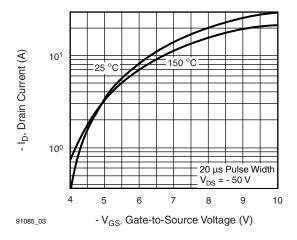


Fig. 3 - Typical Transfer Characteristics

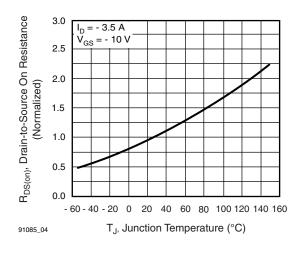


Fig. 4 - Normalized On-Resistance vs. Temperature

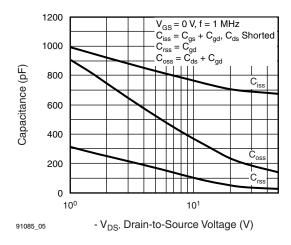


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

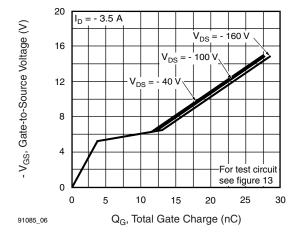


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage



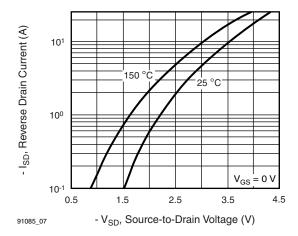


Fig. 7 - Typical Source-Drain Diode Forward Voltage

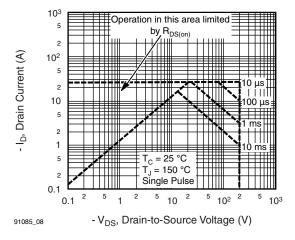


Fig. 8 - Maximum Safe Operating Area

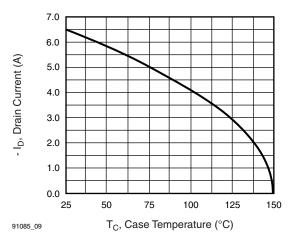


Fig. 9 - Maximum Drain Current vs. Case Temperature

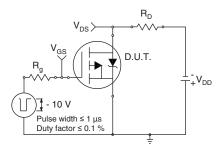


Fig. 10a - Switching Time Test Circuit

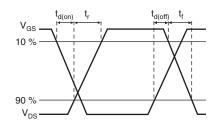


Fig. 10b - Switching Time Waveforms

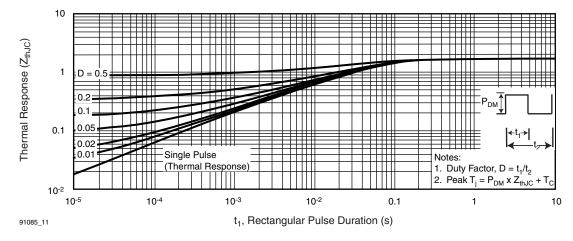


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case



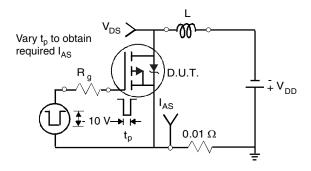


Fig. 12a - Unclamped Inductive Test Circuit

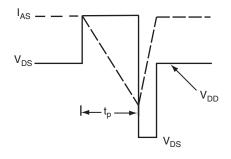


Fig. 12b - Unclamped Inductive Waveforms

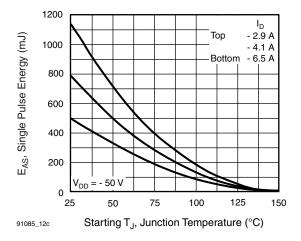


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

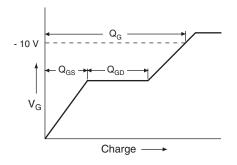


Fig. 13a - Basic Gate Charge Waveform

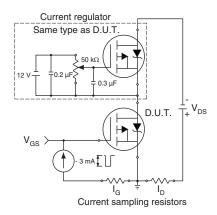
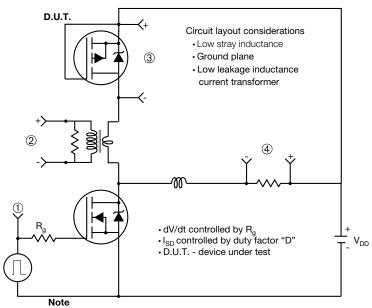


Fig. 13b - Gate Charge Test Circuit

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Peak Diode Recovery dV/dt Test Circuit



• Compliment N-Channel of D.U.T. for driver

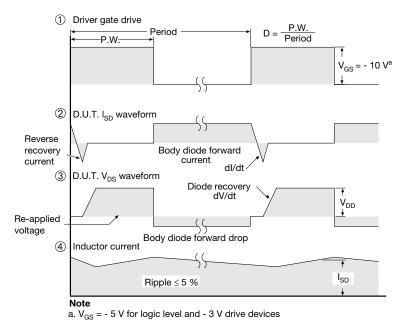
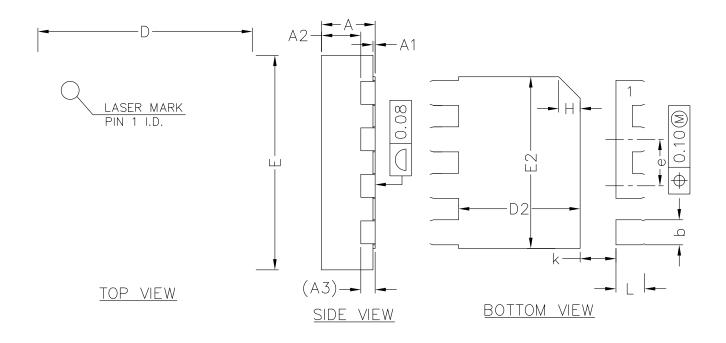
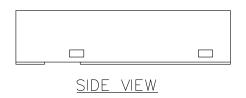


Fig. 14 - For P-Channel

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COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX		
Α	0.70	0.75	0.80		
A1	0.00	0.02	0.05		
A2	0.50	0.55	0.60		
А3	0.20REF				
b	0.30	0.35	0.40		
D	2.90	3.00	3.10		
Е	2.90	3.00	3.10		
D2	1.60	1.70	1.80		
E2	2.30	2.40	2.50		
е	0.55	0.65	0.75		
K	0.40	0.50	0.60		
L	0.35	0.40	0.45		

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