

AON7408-VB Datasheet N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^a	Q _g (Typ.)			
30	0.021 at V _{GS} = 10 V	18	3.8 nC			
30	0.025 at V _{GS} = 4.5 V	17	3.6110			

FEATURES

- Halogen-free According to IEC 61249-2-21
- Trench Power MOSFET
- 100 % R_g Tested



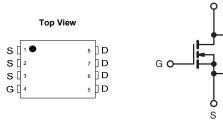
ROHS COMPLIANT

DFN 3x3 EP



APPLICATIONS

- Notebook PC
 - System Power
 - Load Switch



N-Channel MOSFET

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	30	V	
Gate-Source Voltage	V_{GS}	± 20		
Continuous Drain Current (T _J = 150 °C)	$T_C = 25 ^{\circ}C$ $T_C = 70 ^{\circ}C$ $T_A = 25 ^{\circ}C$	I _D	18 ^a 11 ^a 9 ^{b, c}	
T _A = 70 °C Pulsed Drain Current		I _{DM}	7 ^{b, c} 35	Α
Continuous Source-Drain Diode Current	$T_C = 25 ^{\circ}C$ $T_A = 25 ^{\circ}C$	- I _S	12 ^a 2.7 ^{b, c}	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	5	
Single Pulse Avalanche Energy	2 - 0.1 11111	E _{AS}	1.25	mJ
Maximum Power Dissipation		P _D	15.6 10 3.2 ^{b, c} 2 ^{b, c}	w
Operating Junction and Storage Temperature	T _J , T _{stg}	- 55 to 150	°C	
Soldering Recommendations (Peak Tempera		260		

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R_{thJA}	32	39	°C/W		
Maximum Junction-to-Case (Drain)	Steady State	R_{thJC}	6.5	8			

Notes:

- a. Package Limited.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s
- d. Maximum under Steady State conditions is 81 °C/W.
- e. The DFN 3x 3 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- f. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.



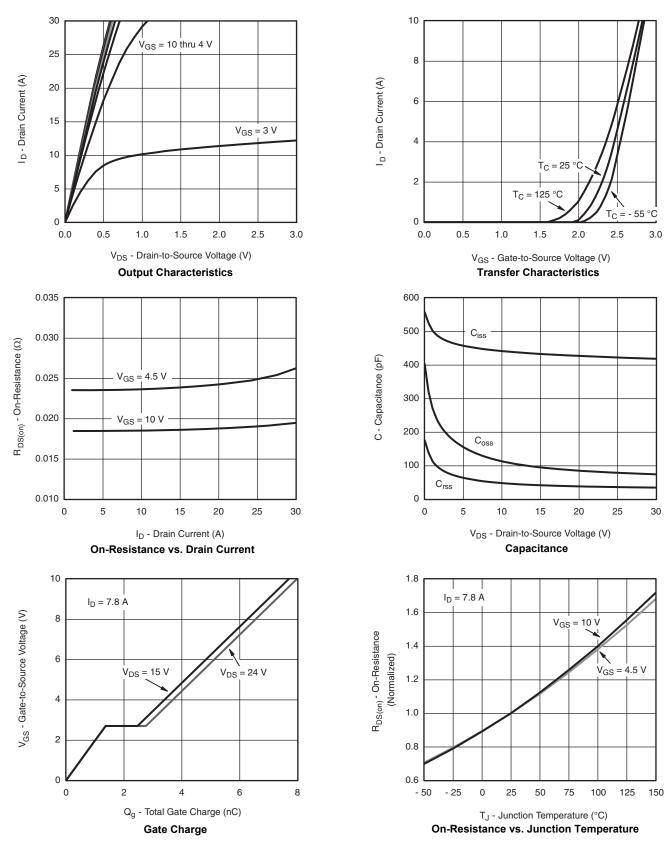
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static	1			1	I.	•
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		35		mV/°C
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 4.5		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	1.0		2.5	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zana Oata Wallana Busin Oamant	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			1	
Zero Gate Voltage Drain Current		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			5	μΑ
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α
_	, ,	$V_{GS} = 10 \text{ V}, I_D = 7.8 \text{ A}$		0.021		Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 7.0 \text{ A}$		0.025		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 7.8 A		17		S
Dynamic ^b	<u> </u>	- -				1
Input Capacitance	C _{iss}			435		pF
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		95		
Reverse Transfer Capacitance	C _{rss}			42		
Total Cata Chausa	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 7.8 \text{ A}$	8	12		
Total Gate Charge				3.8	6	nC
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 7.8 \text{ A}$		1.4		
Gate-Drain Charge	Q_{gd}			1.1		
Gate Resistance	R_{g}	f = 1 MHz	1.5	3.2	4.5	Ω
Turn-On Delay Time	t _{d(on)}			15	25	ns
Rise Time	t _r	V_{DD} = 15 V, R_L = 2.4 Ω		12	20	
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 6.3$ A, $V_{GEN}=4.5$ V, $R_g=1~\Omega$		13	20	
Fall Time	t _f			10	15	
Turn-On Delay Time	t _{d(on)}			5	10	
Rise Time	t _r	V_{DD} = 15 V, R_L = 2.4 Ω		10	15	
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 6.3$ A, V_{GEN} = 10 V, R_g = 1 Ω		15	25	
Fall Time	t _f			10	15	1
Drain-Source Body Diode Characteristi	cs					
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C		12		Α
Pulse Diode Forward Current	I _{SM}				35	
Body Diode Voltage	V_{SD}	I _S = 6.3 A, V _{GS} = 0 V		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}			15	25	ns
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 6.3 A, dl/dt = 100 A/μs, T _J = 25 °C		7	12	nC
Reverse Recovery Fall Time	t _a	1 _F = 0.5 Λ, αι/αι = 100 Α/μs, 1 _J = 25 °C		9		20
Reverse Recovery Rise Time	t _b			6		ns

Notes:

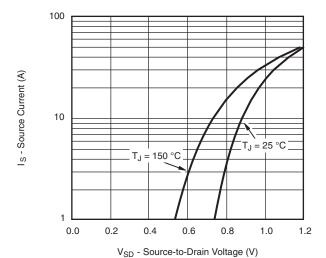
- a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

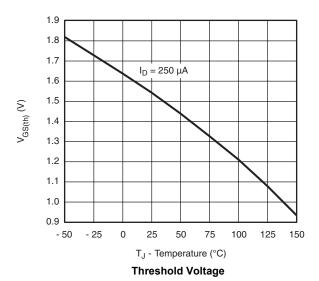






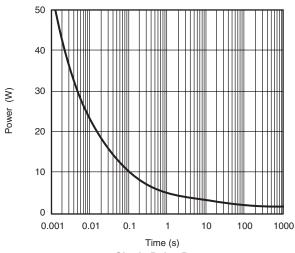


Source-Drain Diode Forward Voltage

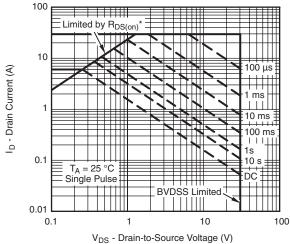


 $C_{\rm S}^{\rm C}$ 0.08 $I_{\rm D} = 7.8~{\rm A}$ $I_{\rm D} = 7.8~{\rm A}$

On-Resistance vs. Gate-to-Source Voltage



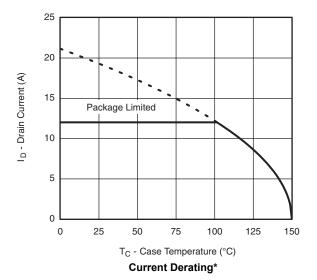
Single Pulse Power

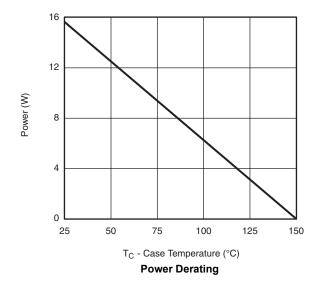


* V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified

Safe Operating Area, Junction-to-Ambient

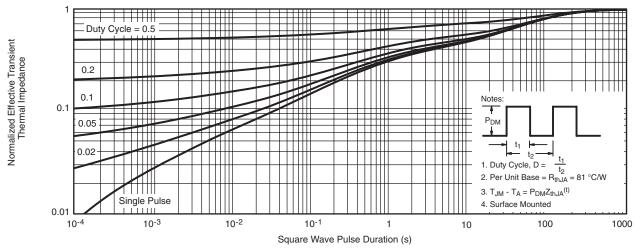




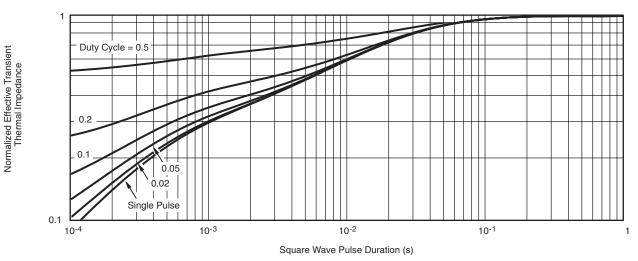


^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





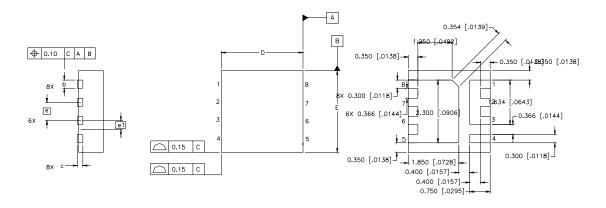
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

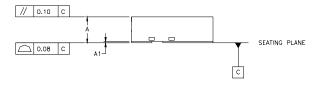


PQFN Package Details



SIDE VIEW TOP VIEW

BOTTOM VIEW



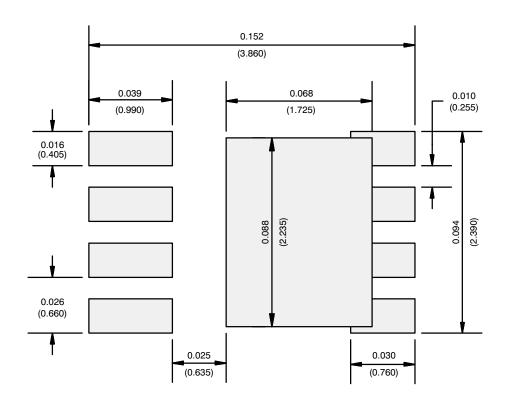
FRONT VIEW

DIM	INCH	IES	MILLIMETERS			
	MIN	MAX	MIN	MAX		
А	.0315	.0394	0.800	1.000		
A1	.0000	.0020	0.000	0.050		
b	.0098	.0138	0.250	0.350		
С	.0080	REF.	0.203	0.203 REF.		
D	.1181 BASIC		3.000	3.000 BASIC		
E	.1181	BASIC	3.000	BASIC		
е	.0262	BASIC	0.666	BASIC		
e1	.0131	BASIC	0.333	BASIC		

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RECOMMENDED MINIMUM PADS



Recommended Minimum Pads Dimensions in Inches/(mm)



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