

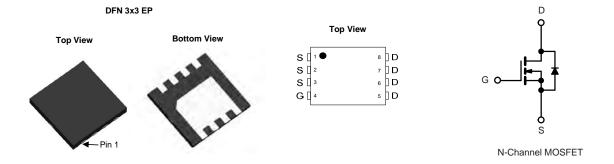
# 076N06N-VB Datasheet N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A) <sup>a</sup>			
60	0.005 at V <sub>GS</sub> = 10 V	30			
60	0.013 at V <sub>GS</sub> = 4.5 V	26			

## **FEATURES**

- 175 °C Junction Temperature
- Trench Power MOSFET
- Material categorization:





<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>C</sub> = 25 °C, unless otherwise noted)						
Parameter	Symbol	Limit	Unit			
Gate-Source Voltage	$V_{GS}$	± 20	V			
Continuous Drain Current (T <sub>.I</sub> = 175 °C) <sup>b</sup>	T <sub>C</sub> = 25 °C	- I <sub>D</sub>	30			
Continuous Diain Current (1 <sub>J</sub> = 175 °C) <sup>2</sup>	T <sub>C</sub> = 100 °C		25 <sup>a</sup>			
Pulsed Drain Current	I <sub>DM</sub>	100	Α			
Continuous Source Current (Diode Conduction)	I <sub>S</sub>	70 <sup>a</sup>				
Avalanche Current	I <sub>AS</sub>	50	]			
Single Avalanche Energy (Duty Cycle ≤ 1 %)	L = 0.1 mH	E <sub>AS</sub>	125	mJ		
Maximum Power Dissipation	T <sub>C</sub> = 25 °C	P <sub>D</sub>	136	W		
Maximum Fower Dissipation	T <sub>A</sub> = 25 °C	' D	3 <sup>b</sup> , 8.3 <sup>b, c</sup>	VV		
Operating Junction and Storage Temperature Range	•	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 10 sec	R <sub>thJA</sub>	15	18		
waximum junction-to-ambient**	Steady State		40	50	°C/W	
Maximum Junction-to-Case		$R_{thJC}$	0.85	1.1		

#### Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.
- $c.\ t \leq 10\ s.$

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Parameter	Symbol	Test Conditions	Min.	Typ a	Max.	Unit	
Static	Symbol Test Conditions Min. Typ.a Max.		Max.	Onic			
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	60				
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	1	2	3	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V, } V_{GS} = \pm 20 \text{ V}$	00 5 1		± 100	nA	
Cate Body Leakage	1635	$V_{DS} = 60 \text{ V}, V_{GS} = 20 \text{ V}$			1	ПА	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 \text{ °C}$			50		
Zelo Gale voltage Diam Guitem	1088	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 \text{ °C}$			250	μΑ	
On-State Drain Current <sup>b</sup>	lp()	$V_{DS} = 5 \text{ V}, V_{GS} = 6 \text{ V}, V_{T} = 176 \text{ C}$	60		230	A	
On-State Drain Current	I <sub>D(on)</sub>	$V_{GS} = 10 \text{ V}, V_{GS} = 10 \text{ V}$	00	0.005			
		$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}, T_J = 125 ^{\circ}\text{C}$		0.003	-		
Drain-Source On-State Resistance <sup>b</sup>	R <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}, T_J = 175 \text{ °C}$		0.010		Ω	
		$V_{GS} = 4.5 \text{ V}, I_D = 15 \text{ A}$		0.013			
Forward Transconductance <sup>b</sup>	O.	$V_{DS} = 15 \text{ V}, I_D = 20 \text{ A}$		60		S	
Dynamic Dynamic	9 <sub>fs</sub>	VDS = 13 V, 1B = 20 A		00			
Input Capacitance	C <sub>iss</sub>		<u> </u>	2650			
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		470		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	1GS 0 1, 1DS 20 1, 1 111112		225		Pi	
Total Gate Charge <sup>c</sup>	Qg			47	70		
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 50 \text{ A}$		10	70	nC	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>	105 00 1, 165 10 1, 10 00 /1		12		110	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			10	20		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 30 \text{ V}, R_1 = 0.6 \Omega$		15	25		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$V_{DD} = 30 \text{ V}, \ N_L = 0.0 \Omega$ $I_D \cong 50 \text{ A}, \ V_{GEN} = 10 \text{ V}, \ R_a = 2.5 \Omega$		35	50	ns	
Fall Time <sup>c</sup>	t <sub>f</sub>	D 1119, 19EN 1119 210 22		20	30		
Source-Drain Diode Ratings and Cha		T <sub>o</sub> = 25 °C)		20	30		
Pulsed Current	I <sub>SM</sub>	10 - 20 - 0)			60	A	
	V <sub>SD</sub>			1.5	V		
Diode Forward Voltage	Ven	$I_F = 20 \text{ A, di/dt} = 100 \text{ A/µs}$ 45					

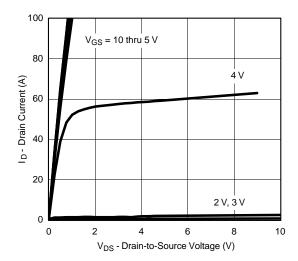
#### Notes:

- a. For design aid only; not subject to production testing.
- b. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- c. Independent of operating temperature.

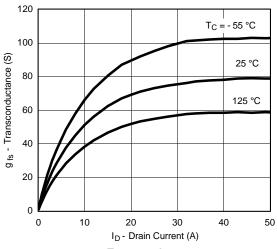
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



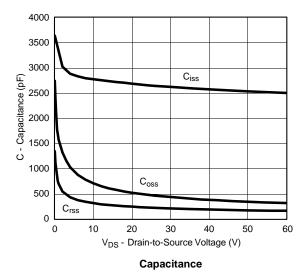
## TYPICAL CHARACTERISTICS (25 °C unless noted)

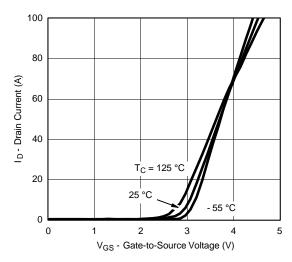


### **Output Characteristics**

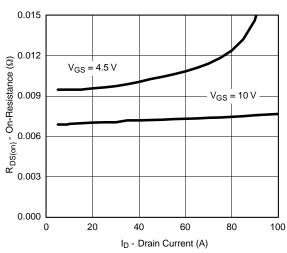


#### Transconductance

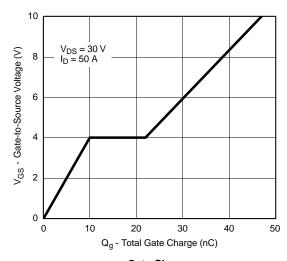




**Transfer Characteristics** 



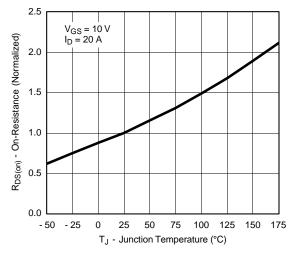
On-Resistance vs. Drain Current



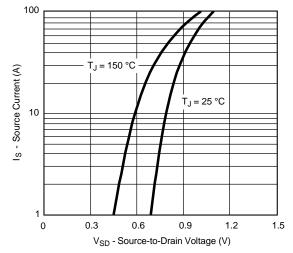
Gate Charge



# TYPICAL CHARACTERISTICS (25 °C unless noted)



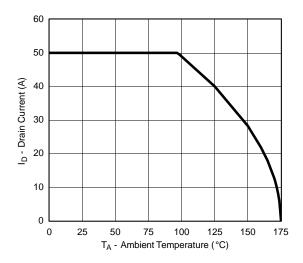
On-Resistance vs. Junction Temperature

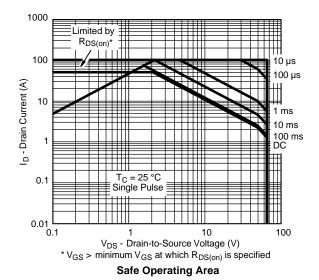


Source-Drain Diode Forward Voltage

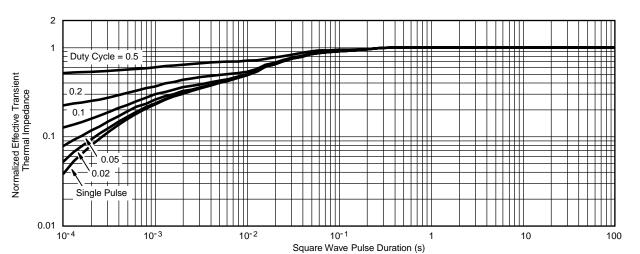


### THERMAL RATINGS





Maximum Drain Current vs. Ambient Temperature

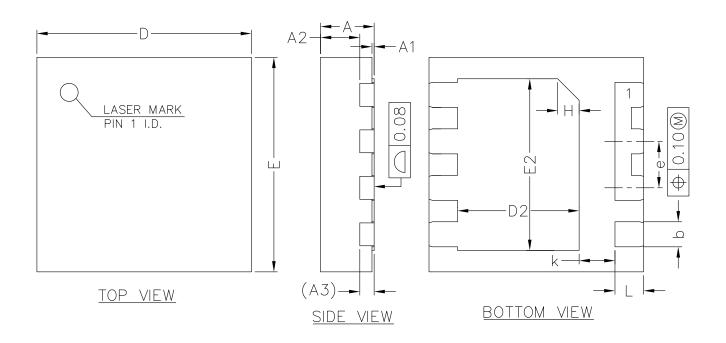


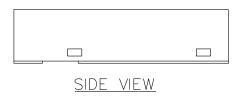
Normalized Thermal Transient Impedance, Junction-to-Case

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COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX	
А	0.70	0.75	0.80	
A1	0.00	0.02	0.05	
A2	0.50	0.55	0.60	
А3	0.20REF			
b	0.30	0.35	0.40	
D	2.90	3.00	3.10	
E	2.90	3.00	3.10	
D2	1.60	1.70	1.80	
E2	2.30	2.40	2.50	
е	0.55	0.65	0.75	
K	0.40	0.50	0.60	
L	0.35	0.40	0.45	



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