

AON3816-VB Datasheet Dual N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)	Q _g (TYP.)			
	0.0170 at V _{GS} = 4.5 V	20				
20	0.0240 at V _{GS} = 2.5 V	17	12 nC			
	0.0490 at V _{GS} = 1.8 V	10				

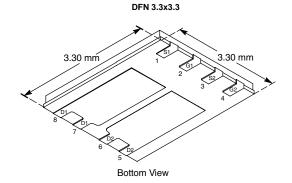
FEATURES

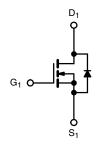
• Trench power MOSFET

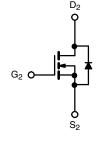
APPLICATIONS

- DC/DC
- Notebook system power
- POL









N-Channel MOSFET

N-Channel MOSFET

PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-Source Voltage Gate-Source Voltage		V _{DS}	20	V
		V _{GS}	± 8	
	T _C = 25 °C		20	
Continuous Dusin Comment (T. 150 °C)	T _C = 70 °C	1 . 🗀	15.8	
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	I _D	8 a, b	
	T _A = 70 °C		6.5 ^{a, b}	
Pulsed Drain Current		I _{DM}	40	Α
Continuous Source-Drain Diode Current	T _C = 25 °C		15	
	T _A = 25 °C	l _s	2.2 ^{a, b}	
Single Pulse Avalanche Current		I _{AS}	15	
Single Pulse Avalanche Energy L = 0.1 mH		E _{AS}	11	mJ
	T _C = 25 °C		20	
Marrian de Derrar Dissination	T _C = 70 °C	1 , [12.8	14/
Maximum Power Dissipation	T _A = 25 °C	P _D	2.5 ^{a, b}	W
	T _A = 70 °C		1.6 ^{a, b}	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +150	°C
Soldering Recommendations (Peak Temperature) c, d			260	- °C

THERMAL RESISTANCE RATINGS							
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT			
Maximum Junction-to-Ambient	t ≤ 10 s	R _{thJA}	38	48	°C/W		
Maximum Junction-to-Case (Drain)	Steady State	R_{thJC}	4.3	5.4	C/VV		

Notes

- a. Package limited, T_C = 25 °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s
- d. Maximum under Steady State conditions is 110 °C/W.



PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static			•	<u>'</u>	I.	
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20	-	-	V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	DS/TJ		22	-	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA	-	-3	-	mV/°C
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	0.4	-	1	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$	-	-	± 100	nA
Zara Cata Valtaga Drain Current		V _{DS} = 20 V, V _{GS} = 0 V	-	-	1	μΑ
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0 V, T _J = 55 °C	-	-	10	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	20	-	-	Α
		V _{GS} = 4.5 V, I _D = 10 A	-	0.0170	-	1
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 2.5 V, I _D = 9 A	-	0.0240	-	Ω
		$V_{GS} = 1.8 \text{ V}, I_D = 8.2 \text{ A}$	-	0.0490	-	1
Forward Transconductance ^a g_{fs} $V_{DS} = 10 \text{ V}, I_D = 10 \text{ A}$		-	47	-	S	
Dynamic ^b						
Input Capacitance	C _{iss}		-	1120	-	
Output Capacitance	Coss	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	180	-	pF
Reverse Transfer Capacitance	C _{rss}		-	80	-	
'	_	V _{DS} = 15 V, V _{GS} = 8 V, I _D = 10 A	-	21	32	nC
Total Gate Charge	Q_g		-	12	18	
Gate-Source Charge	Q _{qs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$	-	2	-	
Gate-Drain Charge	Q _{gd}		-	1.3	-	
Gate Resistance	R_{g}	f = 1 MHz	-	1.8	3.6	Ω
Turn-On Delay Time	t _{d(on)}		-	10	15	
Rise Time	t _r	$V_{DD} = 10 \text{ V}, R_L = 1.25 \Omega$	-	10	15	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 8 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$	-	35	55	
Fall Time	t _f		-	10	15	
Turn-On Delay Time	t _{d(on)}		-	10	15	ns
Rise Time	t _r	V_{DD} = 10 V, R_L = 1.25 Ω	-	10	15	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 8 \text{ A}, V_{GEN} = 8 \text{ V}, R_g = 1 \Omega$	-	25	40	
Fall Time	t _f		-	10	15	
Drain-Source Body Diode Characteristi	cs			I.	ı	
Continuous Source-Drain Diode Current	Is	T _C = 25 °C	-	-	19	
Pulse Diode Forward Current	I _{SM}	-	-	-	40	A
Body Diode Voltage	V _{SD}	I _S = 8 A, V _{GS} = 0 V	-	0.81	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	. 22	-	20	30	ns
Body Diode Reverse Recovery Charge	Q _{rr}		-	15	25	nC
Reverse Recovery Fall Time	ta	$I_F = 8 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$	-	12.5	-	
Reverse Recovery Rise Time	t _b			7.5	_	ns

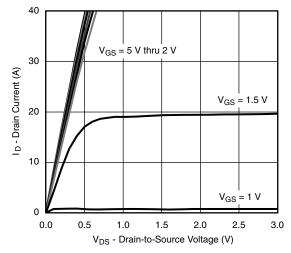
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. Guaranteed by design, not subject to production testing.

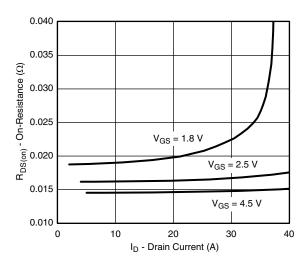
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



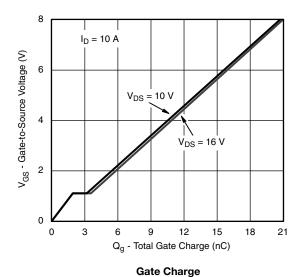
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

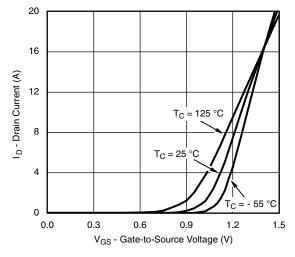


Output Characteristics

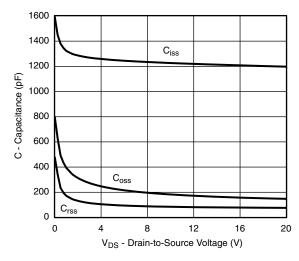


On-Resistance vs. Drain Current and Gate Voltage

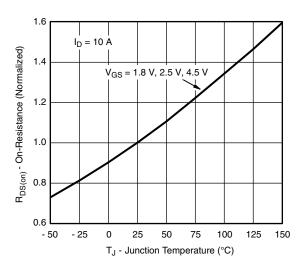




Transfer Characteristics



Capacitance



On-Resistance vs. Junction Temperature

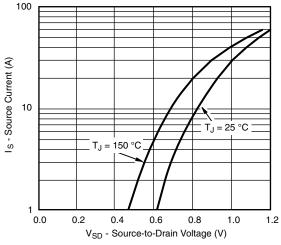


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 $T_J = 125$ °C

 $T_J = 25$ °C

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Source-Drain Diode Forward Voltage
On-Resistance vs. Gate-to-Source Voltage

0.05

0.04

0.03

0.02

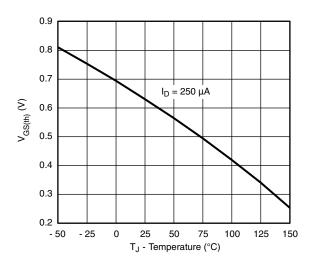
0.01

0

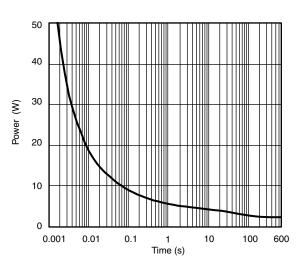
0

 $R_{DS(on)}$ - On-Resistance (Ω)

 $I_D = 10 A$



Threshold Voltage

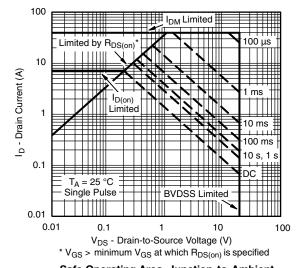


2

3

V_{GS} - Gate-to-Source Voltage (V)

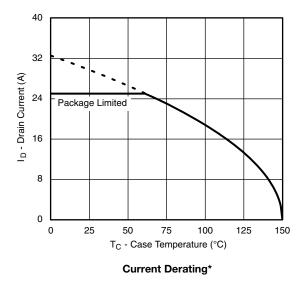
Single Pulse Power, Junction-to-Ambient

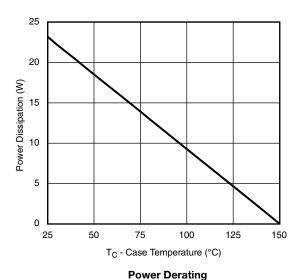


Safe Operating Area, Junction-to-Ambient



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

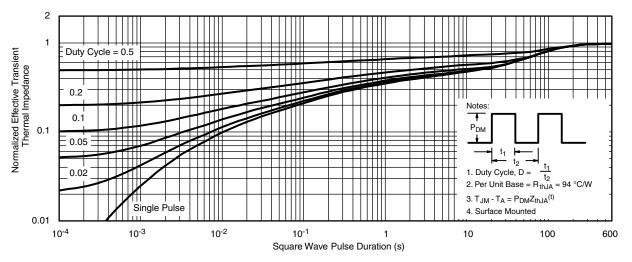




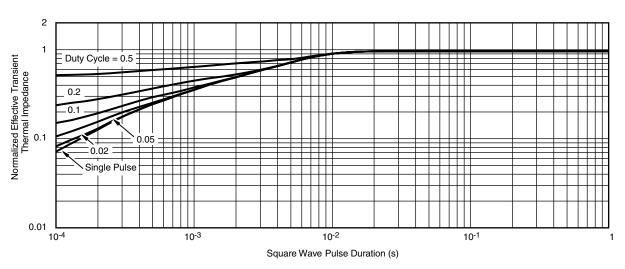
 * The power dissipation P_D is based on T_J (max.) = 150 $^{\circ}$ C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



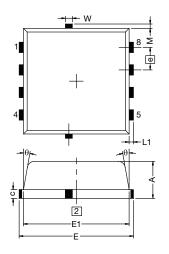
Normalized Thermal Transient Impedance, Junction-to-Ambient

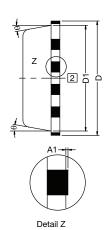


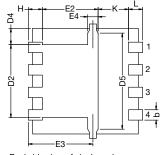
Normalized Thermal Transient Impedance, Junction-to-Case



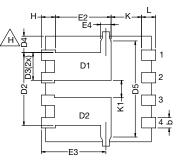
DFN3.3X3.3 (Dual)







Backside view of single pad



Backside view of dual pad

Notes
1. Inch will govern
2 Dimensions exclusive of mold gate burrs
3. Dimensions exclusive of mold flash and cutting burrs

DIM	MILLIMETERS			INCHES			
DIM.	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	
Α	0.97	1.04	1.12	0.038	0.041	0.044	
A1	0.00	-	0.05	0.000	-	0.002	
b	0.23	0.30	0.41	0.009	0.012	0.016	
С	0.23	0.28	0.33	0.009	0.011	0.013	
D	3.20	3.30	3.40	0.126	0.130	0.134	
D1	2.95	3.05	3.15	0.116	0.120	0.124	
D2	1.98	2.11	2.24	0.078	0.083	0.088	
D3	0.48	-	0.89	0.019	-	0.035	
D4		0.47 typ.		0.0185 typ			
D5		2.3 typ.			0.090 typ		
E	3.20	3.30	3.40	0.126	0.130	0.134	
E1	2.95	3.05	3.15	0.116	0.120	0.124	
E2	1.47	1.60	1.73	0.058	0.063	0.068	
E3	1.75	1.85	1.98	0.069	0.073	0.078	
E4	0.034 typ.			0.013 typ.			
е	0.65 BSC			0.026 BSC			
K		0.86 typ.			0.034 typ.		
K1	0.35	-	-	0.014	=	-	
Н	0.30	0.41	0.51	0.012	0.016	0.020	
L	0.30	0.43	0.56	0.012	0.017	0.022	
L1	0.06	0.13	0.20	0.002	0.005	0.008	
θ	0°	-	12°	0°	-	12°	
W	0.15	0.25	0.36	0.006	0.010	0.014	
М	0.125 typ.			0.005 typ.			

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