

## SiA441DJ-T1-GE3-VB Datasheet

### P-Channel 60 V (D-S) MOSFET

#### PRODUCT SUMMARY

$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ ) Typ.	$I_D$ (A) <sup>d</sup>	$Q_g$ (TYP.)
-60	0.058 at $V_{GS} = -10$ V	-6.5	10.1 nC
	0.070 at $V_{GS} = -4.5$ V	-5.1	

#### FEATURES

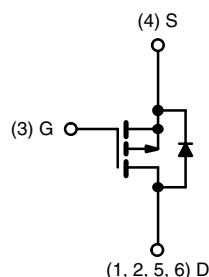
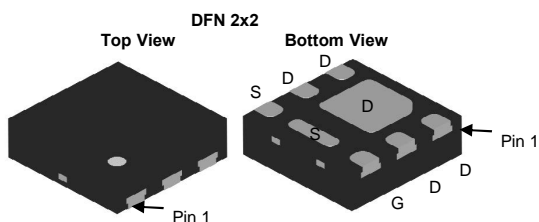
- Trench power MOSFET
- 100 %  $R_g$  and UIS tested

#### APPLICATIONS

- Load switches
- DC/DC converter



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**



P-Channel MOSFET

#### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25$ °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	$V_{DS}$	-60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 150$ °C)	$T_C = 25$ °C	-6.5	A
	$T_C = 70$ °C	-4.5	
	$T_A = 25$ °C	-3.8 <sup>a,b</sup>	
	$T_A = 70$ °C	-3.1 <sup>a,b</sup>	
Pulsed Drain Current ( $t = 100$ $\mu$ s)	$I_{DM}$	-20	
Continuous Source-Drain Diode Current	$T_C = 25$ °C	-3.5	
	$T_A = 25$ °C	-1.7 <sup>a,b</sup>	
Avalanche Current	$L = 0.1$ mH	-15	mJ
Single-Pulse Avalanche Energy	$E_{AS}$	11.25	
Maximum Power Dissipation	$T_C = 25$ °C	4.2	W
	$T_C = 70$ °C	2.7	
	$T_A = 25$ °C	2 <sup>a,b</sup>	
	$T_A = 70$ °C	1.3 <sup>a,b</sup>	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to 150	°C

#### THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum Junction-to-Ambient <sup>a,c</sup>	$R_{thJA}$	40	62.5	°C/W
Maximum Junction-to-Foot	$R_{thJF}$	25	30	

#### Notes

- Surface mounted on 1" x 1" FR4 board.
- $t = 10$  s.
- Maximum under steady state conditions is 110 °C/W.
- Based on  $T_C = 25$  °C.

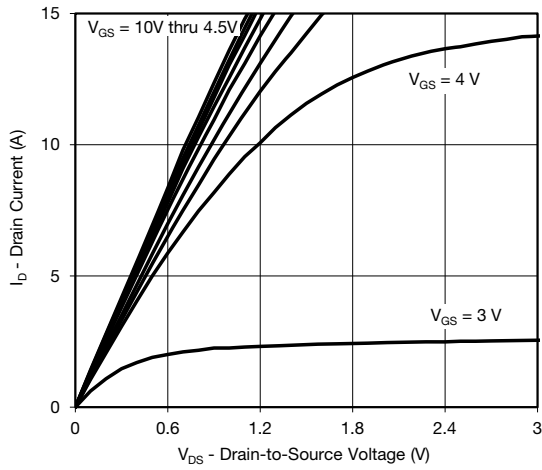
SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = -250 μA	-60	-	-	V
V <sub>DS</sub> Temperature Coefficient	ΔV <sub>DS</sub> /T <sub>J</sub>	I <sub>D</sub> = -250 μA	-	-6.7	-	mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	ΔV <sub>GS(th)</sub> /T <sub>J</sub>		-	4.3	-	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250 μA	-1	-	-3	V
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -60 V, V <sub>GS</sub> = 0 V	-	-	-1	μA
		V <sub>DS</sub> = -60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C	-	-	-5	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ -10 V, V <sub>GS</sub> = -10 V	-30	-	-	A
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = -10 V, I <sub>D</sub> = -3.5 A	-	0.058	-	Ω
		V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -2.8 A	-	0.070	-	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = -30 V, I <sub>D</sub> = -3.5 A	-	11	-	S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0 V, f = 1 MHz	-	832	-	pF
Output Capacitance	C <sub>oss</sub>		-	88	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	63	-	
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = -30 V, V <sub>GS</sub> = -10 V, I <sub>D</sub> = -3.5 A	-	20	30	nC
		V <sub>DS</sub> = -30 V, V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -3.5 A	-	10.1	15.2	
Gate-Source Charge	Q <sub>gs</sub>		-	3.3	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	3.9	-	
Gate Resistance	R <sub>g</sub>	f = 1 MHz	1.8	9	18	Ω
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = -30 V, R <sub>L</sub> = 10.7 Ω I <sub>D</sub> ≡ -2.8 A, V <sub>GEN</sub> = -10 V, R <sub>g</sub> = 1 Ω	-	8	16	ns
Rise Time	t <sub>r</sub>		-	6	12	
Turn-Off DelayTime	t <sub>d(off)</sub>		-	35	53	
Fall Time	t <sub>f</sub>		-	16	24	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = -30 V, R <sub>L</sub> = 10.7 Ω I <sub>D</sub> ≡ -2.8 A, V <sub>GEN</sub> = -4.5 V, R <sub>g</sub> = 1 Ω	-	40	60	
Rise Time	t <sub>r</sub>		-	28	42	
Turn-Off DelayTime	t <sub>d(off)</sub>		-	31	47	
Fall Time	t <sub>f</sub>		-	15	23	
Drain-Source Body Diode Characteristics						
Continous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	-	-	-3.5	A
Pulse Diode Forward Current (t = 100 μs)	I <sub>SM</sub>		-	-	-20	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = -2.8 A, V <sub>GS</sub> = 0 V	-	-0.85	-1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = -2.8 A, dI/dt = 100 A/μs, T <sub>J</sub> = 25 °C	-	32	48	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	45	68	nC
Reverse Recovery Fall Time	t <sub>a</sub>		-	24	-	ns
Reverse Recovery Rise Time	t <sub>b</sub>		-	8	-	

**Notes**

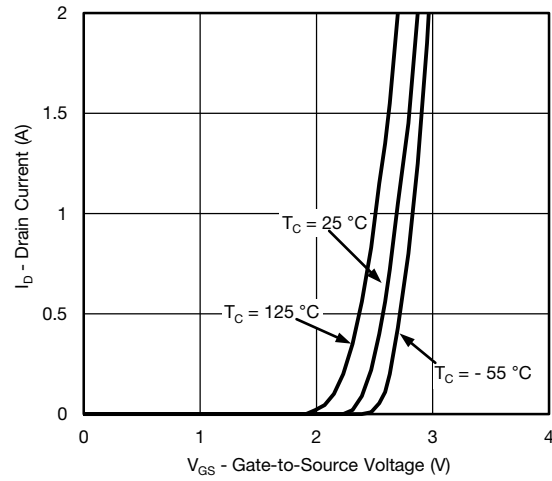
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
 b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

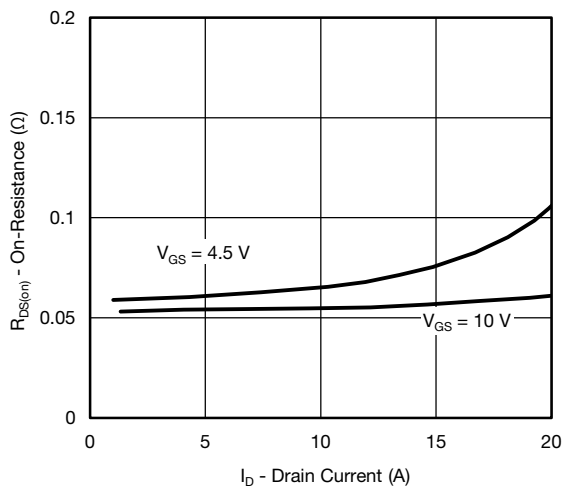
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



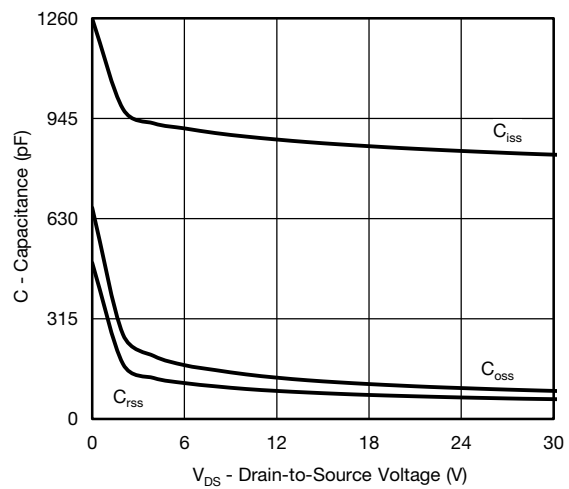
**Output Characteristics**



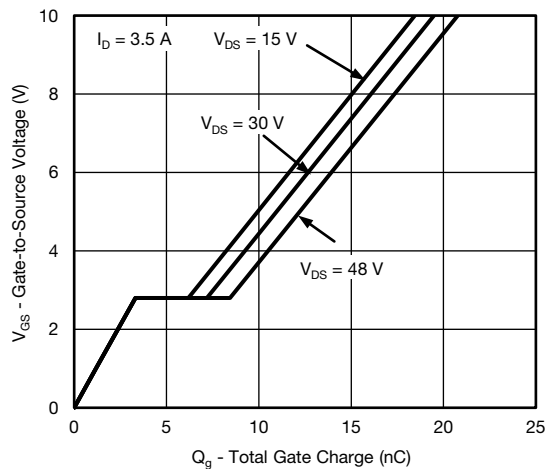
**Transfer Characteristics**



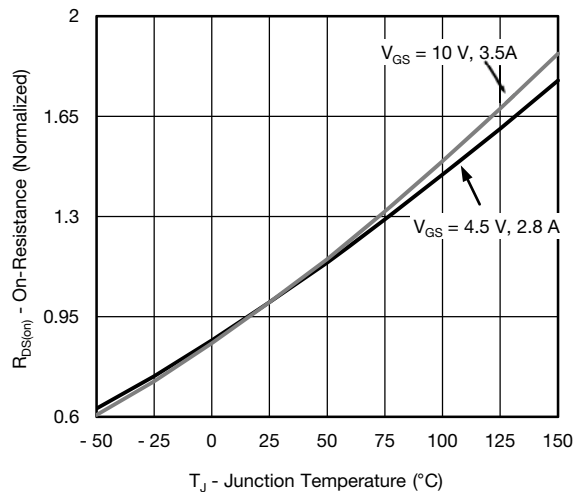
**On-Resistance vs. Drain Current**



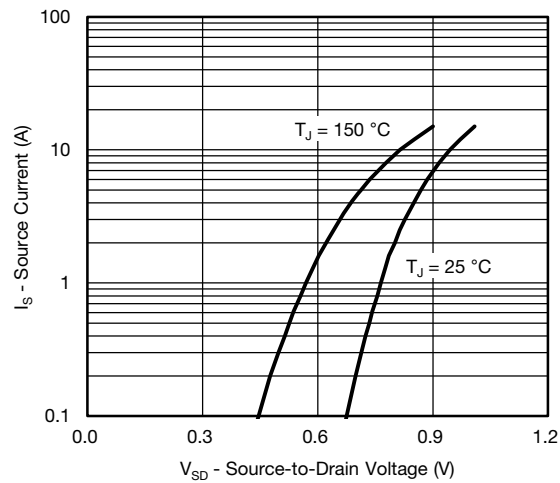
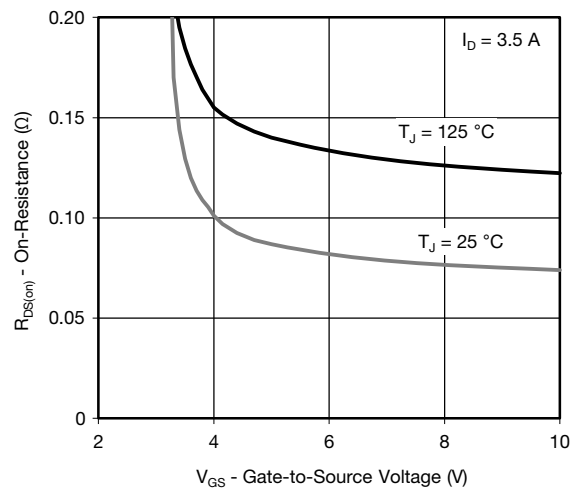
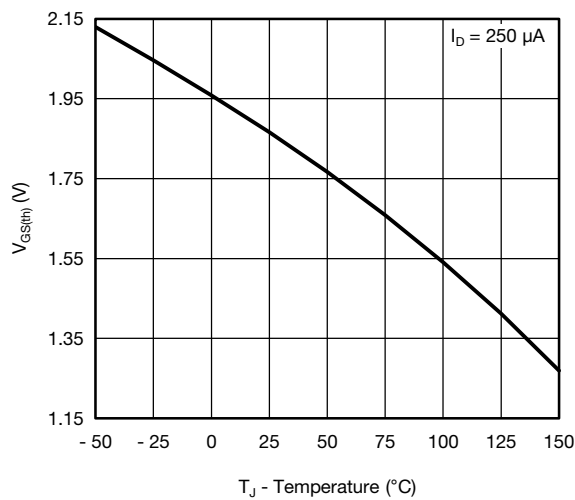
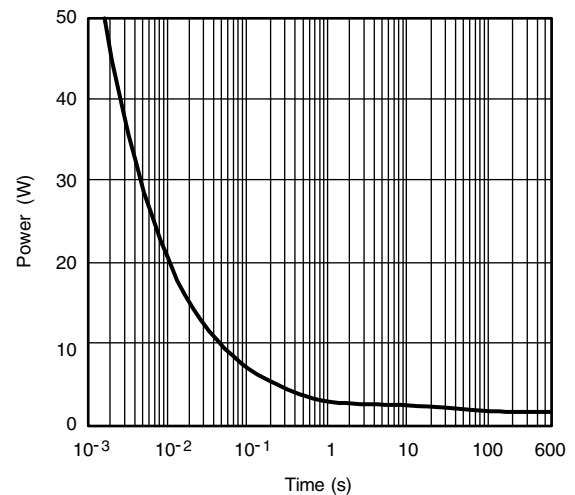
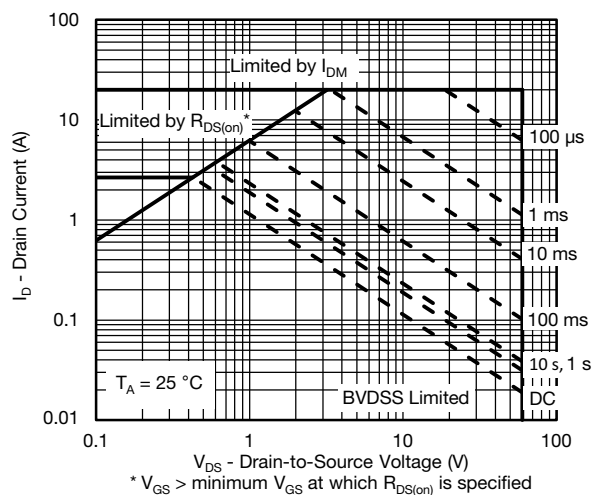
**Capacitance**



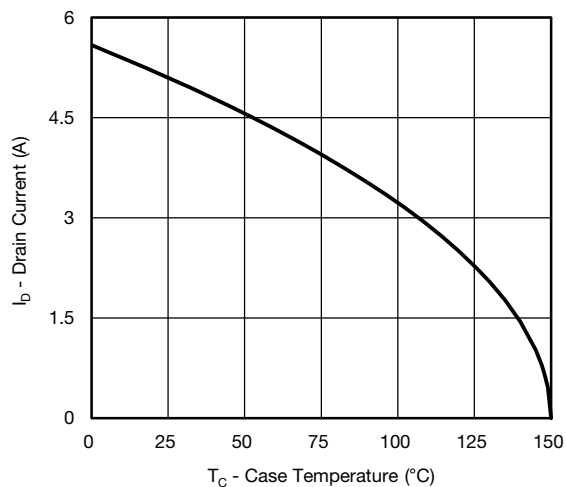
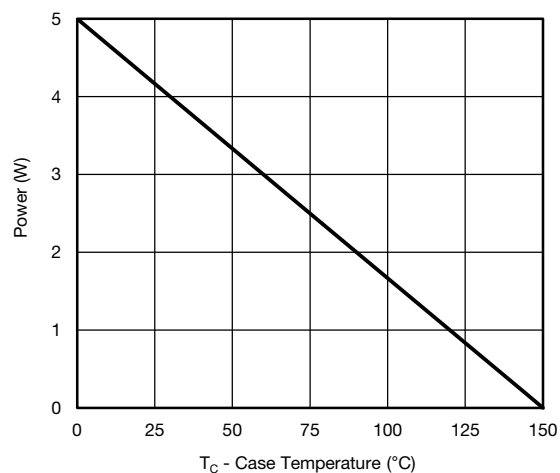
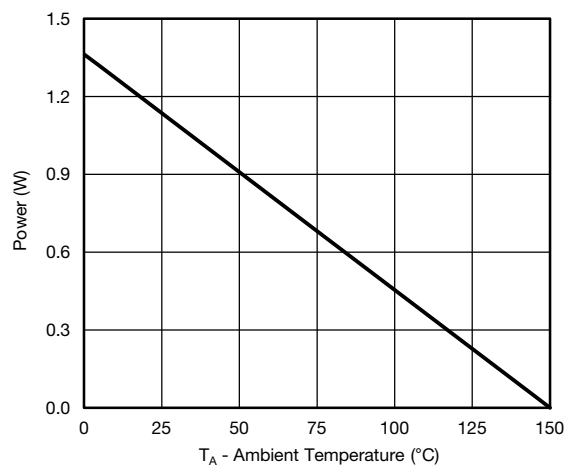
**Gate Charge**



**On-Resistance vs. Junction Temperature**

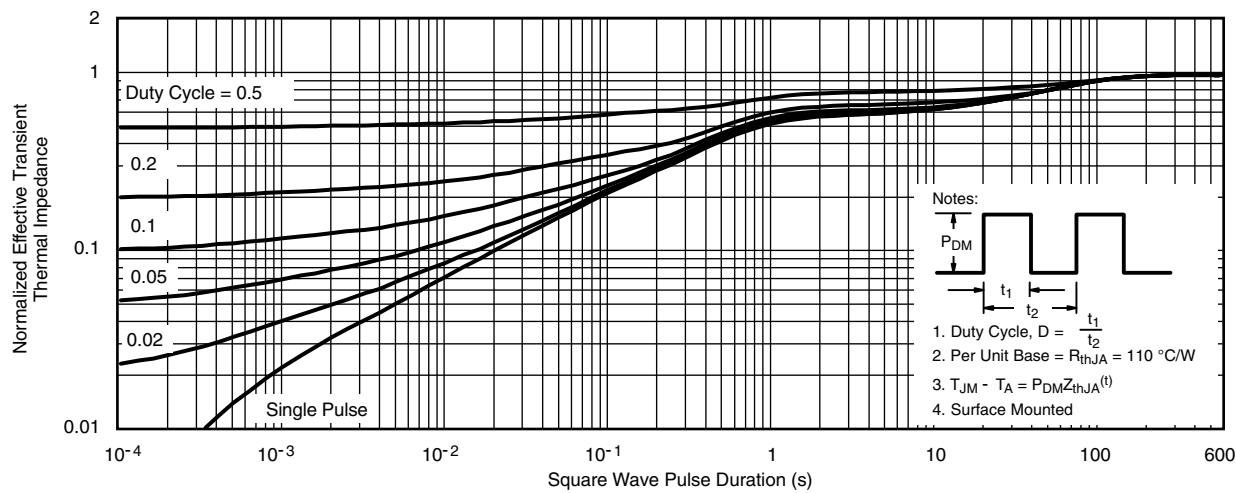
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

**Source-Drain Diode Forward Voltage**

**On-Resistance vs. Gate-to-Source Voltage**

**Threshold Voltage**

**Single Pulse Power, Junction-to-Ambient**

**Safe Operating Area**

\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

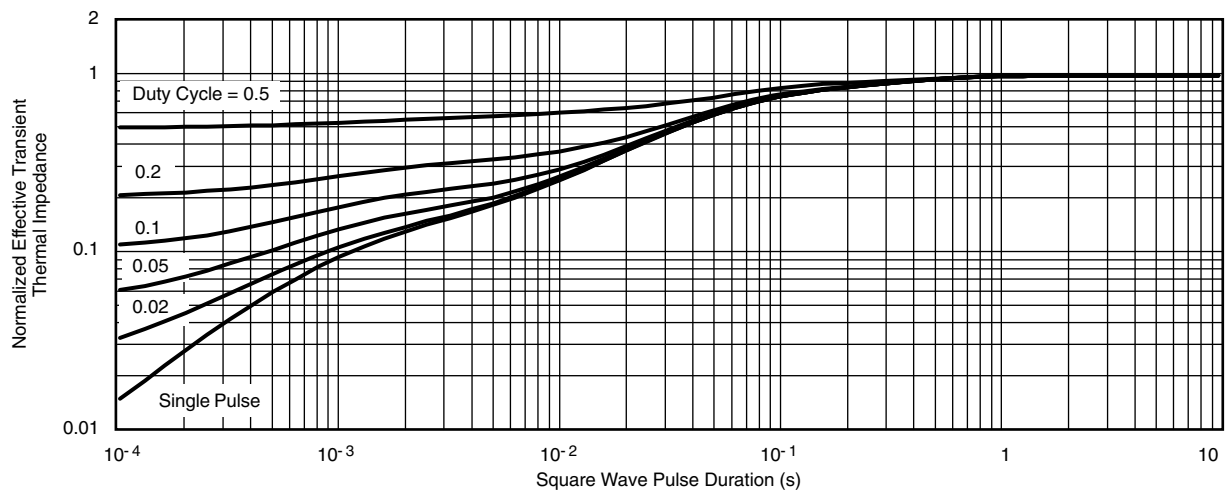
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)**Current Derating\*****Power, Junction-to-Foot****Power Derating, Junction-to-Ambient**

\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150\text{ °C}$ , using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

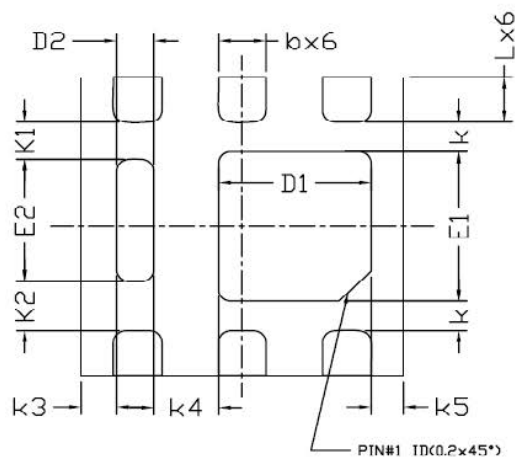
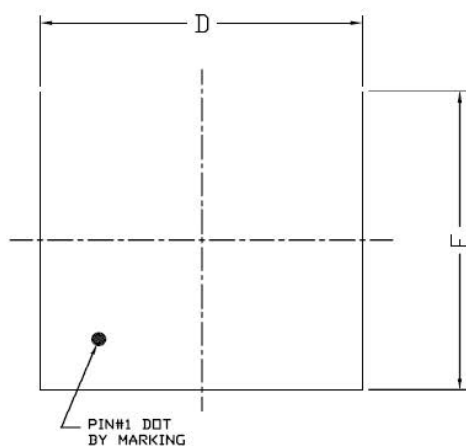


**Normalized Thermal Transient Impedance, Junction-to-Ambient**

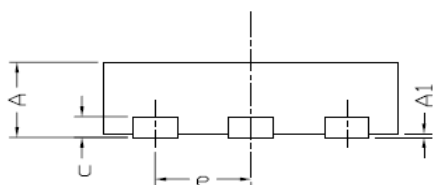


**Normalized Thermal Transient Impedance, Junction-to-Foot**

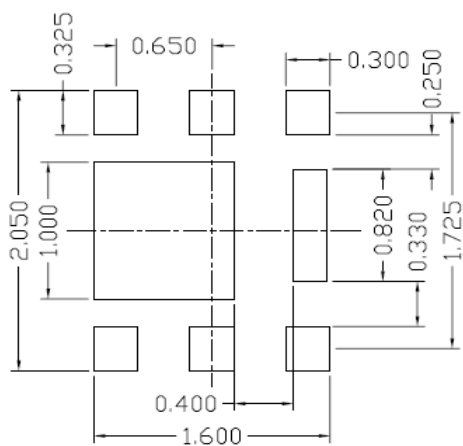
## DFN2x2 \_6L\_EP1\_S PACKAGE OUTLINE



BOTTOM VIEW



## RECOMMENDED LAND PATTERN



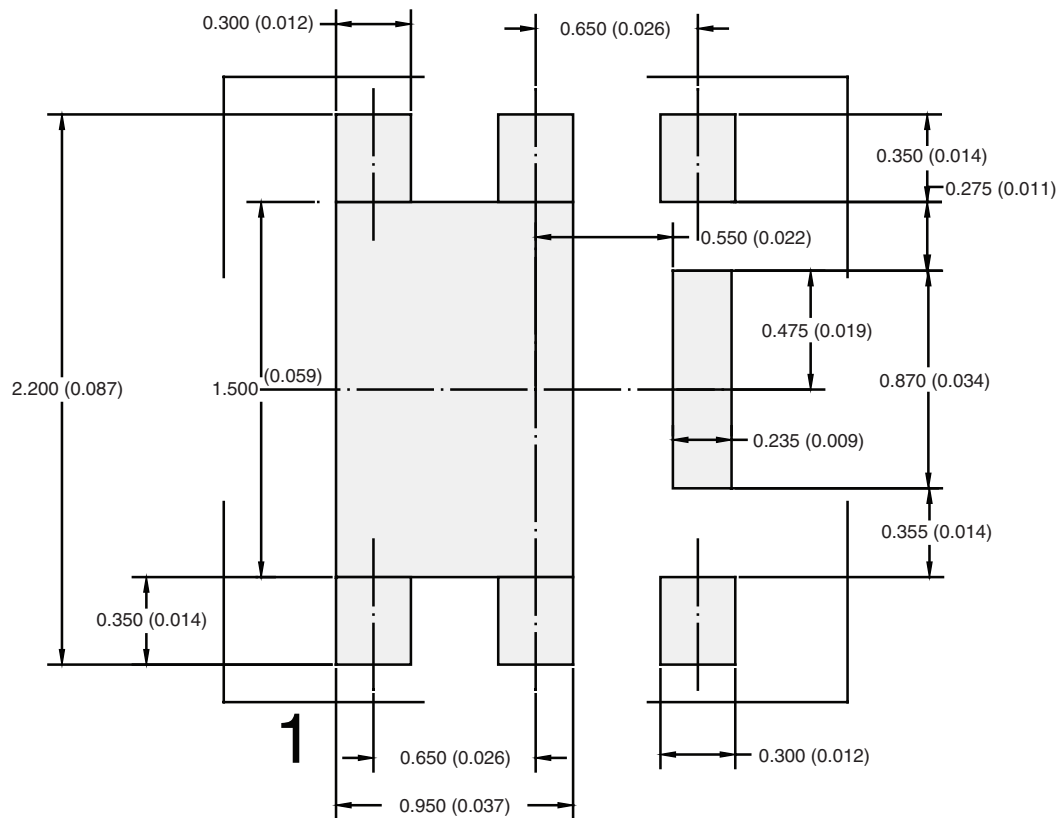
UNIT: mm

## NOTE

1. CONTROLLING DIMENSION IS MILLIMETER.  
 CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.

SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.50	0.55	0.60	0.020	0.022	0.024
A1	0.00	—	0.05	0.000	—	0.002
b	0.25	0.30	0.35	0.010	0.012	0.014
c	0.152 REF			0.006 REF		
D	1.90	2.00	2.10	0.075	0.079	0.083
D1	0.85	0.95	1.05	0.033	0.037	0.041
D2	0.13	0.23	0.33	0.005	0.009	0.013
E	1.90	2.00	2.10	0.075	0.079	0.083
E1	0.90	1.00	1.10	0.035	0.039	0.043
E2	0.72	0.82	0.92	0.028	0.032	0.036
e	0.65 BSC			0.026 BSC		
K	0.20 BSC			0.008 BSC		
K1	0.25 BSC			0.010 BSC		
K2	0.33 BSC			0.013 BSC		
K3	0.22 BSC			0.009 BSC		
K4	0.40 BSC			0.016 BSC		
K5	0.20 BSC			0.008 BSC		
L	0.25	0.30	0.35	0.010	0.012	0.014

## RECOMMENDED PAD LAYOUT FOR DFN2X2



Dimensions in mm/(Inches)



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